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IDENTIFICATION

PRODUCT CODE: AC-E950B-MC
PRODUCT NAME: CXKMCB0 KMC-11 MODULE
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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MAIN DEC CHANGE NOTICE
MAY BE REQUIRED FOR
PROGRAM TO OPERATE

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1. ABSTRACT

KMC IS AN IOMOD THAT EXERCISES UP TO AND INCLUDING TWO CONSECUTIVELY ADDRESSED AND CONSECUTIVELY VECTORED KMC11 SYNCHRONOUS INTERFACES. IT USES NO LINE BUFFERS FOR RECEIVING AND TRANSMITTING DATA. DATA BUFFERS FOR TRANSMITTED AND RECEIVED FROM POP11 MEMORY TO KMC11 AND VICE VERSA, AT THE RECEIVER AND TRANSMITTER ISRS ARE PERFORMED AT LEVEL 0 (P10). DATA CHECKING IS PERFORMED AT LEVEL 0 AND DONE OUTSIDE THE ISRS.

2. REQUIREMENTS

HARDWARE: AT LEAST 1 KMC11

STORAGE:: KMC REQUIRES:

- 1. DECIMAL WORDS: 2235
- 2. OCTAL WORDS: 04273
- 3. OCTAL BYTES: 10566

3. PASS DEFINITION

ONE PASS OF THE KMAA MODULE CONSISTS OF TRANSMITTING AND RECEIVING 1 BUFFERS OF 2-512 CHARACTERS 200 TIMES FOR EACH SELECTED DEVICE.

4. EXECUTION TIME

RUNNING ALONE ON AN 11/45 ONE PASS TAKES APPROXIMATELY ONE MINUTE. IF RUN AT XX BAUD AND XX BUFFER SIZE

5. CONFIGURATION PARAMETERS.

DEFAULT PARAMETERS:
ADDR: 1. VECTOR: 1, BR1: 5, BR2: 5, DVID1: 1 AND SRI:0
KMAA WILL RUN UP TO TWO CONSECUTIVELY ADDRESSED AND
CONSECUTIVELY VECTORED KMC11'S. THERE ARE THREE
PARAMETERS WHICH CAN BE CONTROLLED IN THIS MODULE.
1. NPR RATE: - THIS CONTROLS THE RATE OF NPR'S
OCCURRING FROM KMC11'S. USING MODIFY COMMAND THIS CAN BE
SET TO SPECIFIC VALUE. THE ADDRESS OF THIS PARAMETER IS
226 IN KMAA MODULE.
THIS PARAMETER CAN BE CHOSEN IN TWO DIFFERENT WAYS.
I. WHEN SRI(15)=1 THEN WHATEVER IN LOC 222IRIMULVJ
IS LOADED INTO NPRATE LOC 226J.
II. WHEN SRI(14)=1 THEN NPRATE BECOMES EQUAL TO
RIMULV MULTIPLIED BY SRI(6:11).
DEFAULT:: 10000(OCTAL)
RANGE:: 12-7888 USEC/NPR.

2. NPR/BR RATE: - CONTROLS THE RATE OF NPR'S PER INTERRUPT.
LIKE NPR RATE THIS PARAMETER CAN ALSO BE CHOSEN IN TWO
DIFFERENT WAYS. IN THIS CASE SZMULV(220J) WILL BE USED
IN PLACE OF RIMULV. AND SRI(0:5) CONTENTS WILL BE USED TO MULTIPLY.
PARAMETERS :: RSIZE(202), XSIZE(204)

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DEFAULT:: 377(OCTAL)

RANGE:: 1-377(OCTAL)
CAN BE SET USING MOD COMMAND
3: DIRECTION OF NPR'S: - THE DIRECTION OF NPR'S
CAN BE CHOSEN SETTING PROPER BIT IN SRI.
EXPLANATION FOLLOWS.

6. DEVICE/OPTION SETUP

SRI(SWITCH REGISTER CONTENTS)	OPTION.
BIT15:1 I.E SRI:10XXXX	NPRATE= RTMULV * SRI <6:11>
BIT14:1 I.E SRI:04XXXX	NPR/BR:= RTMULV * SRI <0:5>
BIT 15 & 14:0	NPRATE:= RTMULV
BIT 15 & 14:1	NPR/BR:= SZMULV
BIT13:1 I.E SRI:X2XXXX	DEFAULT RATE.
BIT12:1 I.E SRI:X1XXXX	ILLEGAL.
BIT13 & 12:0	XMIT ONLY RECEIVE ONLY
BIT13 & 12:1	DEFAULT.
SRI BITS 6:11	ILLEGAL MULTIPLIER.
SRI BITS 5:0	NPR RATE MULTIPLIER.

NOTE: SRI CAN BE SET UP AT CONFIGURATION TIME OR
AT RUN TIME WITH A MOD COMMAND.

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7.

MODULE OPERATION

1. LOAD SOFTWARE POINTERS IN LINK TABLE. SET PARAMETERS.
2. LOAD VECTORS AND PRIORITIES IN TABLE.
3. LOAD MICRO-CODE/VERIFY IT AND INITIATE IT.
4. ENABLE SELECTED DEVICES.
5. SCAN FOR ALL DEVICES TO FINISH
6. IF NOT DONE GO TO 4. AND DROP HUNG DEVICE.
7. IF HUNG REPORT SO AND DEVICES SELECTED.
8. CHECK DATA ITERATION COUNT
9. IF NOT = 0 GO TO 1
10. SIGNAL ENDPASS.

IISR: INPUT INTERRUPT SERVICE ROUTINE.

01. GET INTERRUPTING KMCSCR.
02. IF RECEIVE BA/CC WAS REQUESTED, LOAD REC BA/CC.
03. IF XMIT BA/CC WAS REQUESTED, LOAD XMIT BA/CC.
04. RTI

OISR: OUTPUT INTERRUPT SERVICE ROUTINE.

01. GET INTERRUPTING KMCSCR
02. IF ERROR REPORT IT AND EXIT.
03. IF XMIT DONE OR REC DONE, SET APPROPRIATE BITS IN
04. RTI

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9. NON-STANDARD PRINTOUTS

IF THE MODULE "HANGS" IN WHICH NOT ALL SELECTED DEVICES
HAVE FINISHED THEN A "HUNG" MESSAGE IS PRINTED OUT.
CHECK THE ENDPASS FLAGS FOR EACH SELECTED DEVICE IN
THE LINK TABLE TO DETERMINE WHICH DEVICE FAILED TO
FINISH AND HOW FAR IT GOT.

FOR EXAMPLE:

THE TWO ENDPASS FLAGS ARE LOCATED IN THE LINK TABLE
(INTLNK) AT THE FOLLOWING LOCATIONS.
XX11:
XX21:

ONLY BITS 0 THRU 3 ARE USED AND ARE DEFINED AS FOLLOWS:
RECEIVE BA/CC'S WERE LOADED.
TRANSMIT BA/CC'S WERE LOADED.
RECEIVE DONE'S WERE RECEIVED.
TRANSMIT DONE'S WERE RECEIVED.

A CORRECT ENDPASS FLAG = 17, WHEN THE ENDPASS FLAGS
= 17 FOR THE SELECTED DEVICES, THE DATA IS CHECKED IF
A "HUNG" MESSAGE IS TYPED, IS RECHECKED, IF
ENDPASS DID NOT FINISH TO FIND WHICH ONE, CHECK BOTH
ENDPASS FLAGS ANY THAT ARE NOT EQUAL TO 17 ARE THE
HUNG DEVICES. CHECK WHICH BITS OF THE ENDPASS FLAG ARE
CLEAR TO SEE WHAT IT WAS TRYING TO DO.

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178
179 000000*
180 000000*
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185 000000*
186 000000* 046513 041103 040
187 000005* 000
188 000005* 000001
189 000010* 000001
190 000012* 240
191 000013* 240
192 000014* 000001
193 000016* 000000
194 000020* 000000
195 000022* 000000
196 000024* 000000
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198 000025* 140000
199 000030* 000272*
200 000032* 000224*
201 000034* 000000
202 000036* 000200
203 000040* 000000
204 000042* 000000
205 000044* 000000
206 000046* 000000
207 000050* 000000
208 000052* 000000
209 000054* 000000
210 000056* 000000
211 000060* 000000
212 000062* 000000
213 000064* 000000
214 000066* 000000
215 000068* 000000
216 000070* 000000
217 000072* 000000
218 000074* 000000
219 000076* 000000
220 000100* 000000
221 000102*
222 000104* 000000
223 000106*
224 000108* 000000
225 000110* 000000
226 000112* 000322*
227 000114* 000000
228 000116* 000000
229 000120* 000000
230 000122* 000136
231 000124*
232
233 000224*

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MODULE 140000,KMCB >1,1,5,5,0,200,136
TITLE KMCB DEC/X11 SYSTEM EXERCISER MODULE
DDXCDM VERSION 6 23-MAY-78
*****
BEGIN:
MODNAM: ASCII /KMCB /;MODULE NAME
XFLAG: BYTE OPEN ;USED TO KEEP TRACK OF WBUF USAGE
ADDR: 1+0 ;1ST DEVICE ADDR.
VECTOR: 1+0 ;1ST DEVICE VECTOR.
BR1: BYTE PRTV5+0 ;1ST BR LEVEL.
BR2: BYTE PRTV5+0 ;2ND BR LEVEL.
DVID1: 0+1 ;DEVICE INDICATOR 1.
SR1: OPEN ;SWITCH REGISTER 1
SR2: OPEN ;SWITCH REGISTER 2
SR3: OPEN ;SWITCH REGISTER 3
SR4: OPEN ;SWITCH REGISTER 4
*****
STAT: 140000 ;STATUS WORD
INT: START ;MODULE START ADDR.
SPOINT: MODSP ;MODULE STACK POINTER.
PASCNT: 0 ;PASS COUNTER.
ICOUNT: 200 ;# OF ITERATIONS PER PASS=200
LCOUNT: 0 ;LOC TO COUNT ITERATIONS
SOF CNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
SDFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
COMFIC: 0 ;RESERVED FOR MONITOR USE
RES1: 0 ;RESERVED FOR MONITOR USE
RES2: 0 ;RESERVED FOR MONITOR USE
SVRO: OPEN ;LOC TO SAVE R0.
SVR1: OPEN ;LOC TO SAVE R1.
SVR2: OPEN ;LOC TO SAVE R2.
SVR3: OPEN ;LOC TO SAVE R3.
SVR4: OPEN ;LOC TO SAVE R4.
SVR5: OPEN ;LOC TO SAVE R5.
SVR6: OPEN ;LOC TO SAVE R6.
CSRA: OPEN ;ADDR OF CURRENT CSP.
SPADR: ;ADDR OF GOOD DATA, OR
ACSR: OPEN ;CONTENTS OF CSR.
WASADR: ;ADDR OF BAD DATA, OR
ASTAT: OPEN ;STATUS REG CONTENTS.
ERRTYP: ;TYPE OF ERROR
ASB: OPEN ;EXPECTED DATA.
AWAS: OPEN ;ACTUAL DATA.
RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
WMDTO: OPEN ;WORDS TO MEMORY PER ITERATION
WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
INTH: OPEN ;# OF INTERRUPTS PER ITERATION
IDNUM: 136 ;MODULE IDENTIFICATION NUMBER=136
MODSP:

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244 000224* 000000
245 000226* 000000
246 000230* 000000
247 000232* 000017
248 000234* 000000
249 000236* 000000
250 000240* 000377
251 000242* 000377
252 000244* 000000
253 000246* 000000
254 000250* 000000
255 000252* 000000
256 000254* 000000
257 000256* 000002
258 000260* 000100
259 000262* 000
260 000263* 000
261
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263 000264* 010000
264 000266* 000000
265 000270* 000000
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281 000272* 005067 177772
282 000276* 032767 177774 177510
283 000304* 001004
284 000306* 016767 177502 177714
285 000314* 001002
286 000316*
287 000316* 104410 000000*
288 000322* 005067 177706
289 000326* 012700 005276*

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*****
VARIABLES FOR KMC11
*****
DLV1: .WORD 0 ;DEVICE 1 DELAY COUNT.
DLV2: .WORD 0 ;DEVICE 2 DELAY COUNT.
SELECT: .WORD 0 ;TEMPORARY SELECTED DEVICE'S
FLAG: .WORD 17 ;END PASS FLAG.
FIRST: .WORD 0 ;FIRST PASS FLAG.
MASK: .WORD 0 ;TEMPORARY VARIABLE.
R1Z: .WORD 377 ;RECEIVE BUFFER SIZE.
XSIZE: .WORD 377 ;TRANSMIT BUFFER SIZE.
VA: .WORD 0 ;VIRTUAL ADDRESS.
PA: .WORD 0 ;PHYSICAL ADDRESS.
EA: .WORD 0 ;EXTENDED ADDRESS.
SAR0: .WORD 0 ;SAVE LOC FOR R0.
SAR1: .WORD 0 ;SAVE LOC FOR R1.
SZMULV: .WORD 2 ;LOCATION USED TO CALCULATE NPR/BR RATE.
RTMULV: .WORD 100 ;LOCATION USED TO CALCULATE NRR RATE.
TERM: 0 ;TERMINATING VALUE.
RCOLY: .BYTE 0 ;XMITR ONLY FLAG=SPAD<16>
XMOLY: .BYTE 0 ;RCV ONLY FLAG=SPAD <15>
NPRTE: .WORD 10000 ;LOCATION FOR NPR RATE.
TEMP: .WORD 0 ;TEMPORARY VARIABLE.
FLAG: .WORD 0 ;USED TO LOAD MAIN MEMORY.
*****
BEGIN THE DEC./X11 MODULE FOR THE KMC11
*****
START: CLR FLAG ;SET FOR FIRST PASS...
BPT #C<3>,DVID1 ;DROP MODULE IF OTHER THEN
BNE DROP ;FIRST 2 DEVICES ARE SELECTED
MOV DVID1,SELECT ;SELECT=ACTIVE DEVICES.
BNE RSTRT ;DROP MODULE IF NO ACTIVE DEVICES.
DROP: ENDS,BEGIN
RSTRT: CLR FIRST ;INITIALIZE THE FIRST TIME FLAG.
LOOP: MOV #RBUF11,R0 ;GET SET TO CLEAR BUFFERS.

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290 000332 005020  
291 000334 020027 007276*  
292 000340 003774  
293 000342 016700 177662  
294 000350 016767  
295 000350 005767 177714  
296 000354 001173  
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1S: CLR (R0)+ ;CLEAR THE LOCATION IN BUFFER.  
CMP R0,#BASE1 ;END OF BUFFERS?  
BLE IS ;NO GO CLEAR THE NEXT...  
MOV DIRECT,R0 ;DROP MODULE IF NO DEVICES SELECTED.  
BND FLAG ;IS IT FIRST PASS???  
TST FLAG ;NO,THEN DONT LOAD THE MICRO-CODE...  
BNE SETUP1 ;NO,THEN DONT LOAD THE MICRO-CODE...  
;*****  
; I. THIS PART SETS UP PARAMETERS COMMON TO ALL  
; DEVICES LIKE: 1)NPR/RR RATE.  
; 2)NPR RATE.  
; 3)DIRECTION OF NPR.  
; II. INITIATES MICRO-CODE LOADED IN KMC11.  
;*****  
000356 105067 177700 CLRBB RCOLV ;INITIALIZE THE FLAGS.  
000362 105067 177675 CLRBB XMOV ;INITIALIZE THE FLAGS.  
000364 005067 003642 CLR X11 ;INITIALIZE THE END PASS FLAG.  
000365 005067 003660 CLR XX2I ;INITIALIZE THE END PASS FLAG.  
000376 005767 177414 TST SR1 ;IS IT MULTIPLY OPTION?  
000402 100416 000000 177404 BIT #BIT14,SR1 ;IS THE RATE SET?  
000402 001467 040000 177404 BEQ 35 ;NO,TAKE DEFAULT.  
000414 016767 177636 177616 MOV S2,MULV,RSIZE ;SET NPR/RR RATE.  
000422 016767 177630 177612 MOV S3,MULV,NPRATE ;SET NPR/RR RATE.PARAMETER.  
000430 016767 177624 177612 MOV R1,MULV,NPRATE ;SET NPR RATE.  
000436 000455 000000 177350 DR 35 ;SET UP THE REST.  
000440 032767 040000 177350 BIT #BIT14,SR1 ;IS SR1<14>=1?  
000446 001061 177342 177336 BNE #SR1,R1 ;YES GO SET UP ERROR & DROP THE MODULE.  
000450 016702 177336 MOV SR1,R2 ;RETRIEVE BUFFER SIZE MULTIPLICAND.  
000460 006202 000000 177504 ASR R2 ;SET UP NPR RATE MULTIPLICAND.  
000462 006202 000000 177504 ASR R2 ;SET UP NPR RATE MULTIPLICAND.  
000466 006202 000000 177504 ASR R2 ;SET UP NPR RATE MULTIPLICAND.  
000470 006202 000000 177504 ASR R2 ;SET UP NPR RATE MULTIPLICAND.  
000472 006202 000000 177504 ASR R2 ;SET UP NPR RATE MULTIPLICAND.  
000474 042701 177700 BIC #177700,R1 ;CLEAR THE EXTRA BITS.  
000500 042702 177700 BIC #177700,R2 ;CLEAR UNNECESSARY BITS.  
000504 010103 177544 MOV R2,R3 ;GET THE MULTIPLICAND...  
000506 016704 003466 MOV S2,MULV,R4 ;GET THE MULTIPLICAND...  
000516 022767 000377 177542 CMP #377,TEMP ;MULTIPLY & RETURN THE RESULT IN TEMP.  
000524 003406 177534 177504 BLE 125 ;CHECK IF WITHIN LIMIT...  
000526 016767 177526 177504 MOV TEMP,RSIZE ;NO TAKE THE DEFAULT...  
000532 010203 177516 177500 MOV TEMP,XSIZE ;XSIZE=NPR/RR RATE.  
000544 016704 177510 177504 MOV R2,R3 ;GET THE MULTIPLICAND...  
000550 004567 003430 177504 MOV R2,MULV,R4 ;GET THE MULTIPLICAND...  
000552 003403 010000 177504 JSC #R3,TEMP ;MULTIPLY & RETURN RESULT IN TEMP.  
000564 016767 177476 177472 BLE 35 ;CHECK IF WITHIN LIMIT...  
000572 032767 010000 177218 MOV TEMP,NPRTE ;NO,TAKE THE DEFAULT...  
BIT #BIT12,SR1 ;NPRTE=NPR/TEMP.  
; IS IT RECEIVE ONLY?
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346 000600 001413 BEQ 55 ;NO, CHECK IF XMITR ONLY.  
347 000602 032767 020000 177206 PIT #PIT13,SR1 ;IS XMITR ONLY ALSO SET?  
348 000610 001404 6S ;NO,SETUP FOR RECEIVE ONLY  
349 000612 104403 000000 002016 4S: MSCNS,BEGIN,SOFT1 ;ASCII MESSAGE CALL WITH COMMON HEADER  
350 000620 000636 BR DROP ;DROP THE MODULE  
351 000622 105167 177434 6S: COMB RCOLV ;SET RECEIVE ONLY FLAG.  
352 000626 000406 BR 7S ;SET UP OTHER VARIABLES.  
353 000630 032767 020000 177160 5S: BIT #BIT13,SR1 ;IS XMITR ONLY SET?  
354 000636 001402 7S ;NO,DO BOTH  
355 000640 105167 177417 COMB XMOV ;SET XMITR ONLY FLAG.  
356 000644 016701 177136 MOV #R1,GET THE ;GET DEVICE CSR.  
357 000650 016703 177334 7S: SELECT,R3 ;GET THE DEVICE SELECTED.  
358 000654 005067 177406 8S: CLP TEMP ;CLEAR THE RETRY COUNT.  
000660 006203 ASR R3 ;ANY DEVICE REMAINS  
000662 103404 BCS 9S ;YES GO AND LOAD MICRO-CODE INTO IT.  
000664 001427 BEQ SETUP1 ;SETUP THE REST.  
000666 062701 000010 10S: ADD #10,R1 ;UPDATE THE CSR.  
000672 000770 RR 8S ;LOAD THE NEXT DEVICE.  
000674 004767 002456 9S: JSR PC,WCRAM ;WRITE THE CRAM WITH MICRO-CODE.  
000676 004767 002644 JSR PC,WEMRY ;AND LOAD LOWER HALF OF MAIN MEMORY WITH XMITR BUFFER.  
000704 004767 002714 JSR PC,VERIFY ;VERIFY MICRO-CODE & XMITR DATA.  
000710 005767 177322 TST MASK ;IS THERE ANY ERROR.  
000714 001764 BEQ 10S ;NO GO INITIATE IT.  
000716 104403 000000 002022 MSCNS,BEGIN,SOFT2 ;ASCII MESSAGE CALL WITH COMMON HEADER  
000724 005267 177336 INC TEMP ;INCREMENT RETRY COUNT  
000730 022767 000003 177330 CMP #3,TEMP ;IS IT TRIED THREE TIMES?  
000736 003356 BGT 9S ;NO,TRY AGAIN!  
000740 000167 177352 JMP DROP ;DROP THE MODULE.  
;*****  
; THIS PART SETS UP THE PROGRAM CONTROL  
; VARIABLES FOR THE DEVICES AND THE  
; MODULE. EX. QUEUES....,ETC.  
;*****  
SETUP1: MOV ADDR,R1 ;GET THE DEVICE CSR.  
VECTOR,R2 ;GET THE VECTOR.  
MOV #INTLNK,R3 ;GET THE POINTER TO INTERRUPT LINKAGE.  
MOV FLAG,XX11 ;SET THE END PASS FLAG FOR DEV1.  
MOV FLAG,XX21 ;SET THE END PASS FLAG FOR DEV2.  
MOV #PIRINQ,INQIN ;SET UP ALL QUEUES & ITS POINTERS.  
MOV #PIRINQ,INQOUT ;SET UP ALL QUEUES & ITS POINTERS.  
MOV #PIROUTQ,OUTQOUT ;SET UP ALL QUEUES & ITS POINTERS.  
MOV #PIROUTQ,OUTQIN ;SET UP ALL QUEUES & ITS POINTERS.  
MOV #REGQ,REGQ1 ;SET UP ALL QUEUES & ITS POINTERS.  
MOV #REGQ,REGQ0 ;SET UP ALL QUEUES & ITS POINTERS.  
MOV SELECT, R0 ;RO= DEVICES SELECTED.  
ASR R0 ;YES GO SET IT UP.  
JCS 4S ;ALL DONE?  
BEQ SETUP2 ;NO,UPDATE CSR.  
ADD #10,R1 ;UPDATE VECTOR  
ADD #10,R2 ;UPDATE VECTOR  
ADD #22,R3 ;UPDATE LINK.
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402 001066* 000766          BR      2$          ;GO SET UP FOR NEXT DEVICE.
403 001070* 010312          MOV     R3,(R2)    ;LOAD INTERRUPT VECTOR ADDRESS.
404 001072* 167614 000002 4$:  MOV     R1,(R3)    ;SET THE PRIORITY.
405 001100* 010763 000010          MOV     R1,(R3)    ;SET THE DEVICE CSR.
406 001104* 010362 000004          ADD     R3,4(R2)   ;LOAD XMITR.
407 001110* 062762 000004 000004  MOV     R4,4(R2)   ;INTERRUPT VECTOR.
408 001116* 167670 000006          MOV     R1,16(R2) ;SET THE PRIORITY.
409 001124* 005063 000012          CLR     12(R3)    ;CLEAR END PASS FLAG FOR DEV#1.
410 001130* 005063 000016          CLR     16(R3)    ;CLEAR THE RECEIVE BUFFER OFFSET.
411 001134* 105767 177123          TSTB   XMOLY      ;IS XMITR ONLY FLAG SET?
412 001140* 001404          BEQB   5$        ;NO CHECK FOR RECEIVE ONLY FLAG.
413 001142* 012763 000012          MOV     #BIT11RIT3,12(R3) ;SET XMITR BITS IN ENDPASS FLAG.
414 001150* 000740          BR      3$        ;CONTINUE.
415 001152* 105767 177104          TSTB   RCOLY      ;IS RECEIVE ONLY FLAG SET?
416 001156* 001735          BEQB   3$        ;NO DONT SET ANY BITS IN ENDPASS FLAG.
417 001160* 012763 000012          MOV     #ITO1BIT2,12(R3) ;SET RECEIVE BITS IN ENDPASS FLAG.
418 001166* 016701 000005 000012  SETUP2: MOV     ADDR,R1 ;R1=CSR ADDRESS.
419 001172* 016700 177032          MOV     SELECT,R0 ;R0=SELECT.
420 001176* 006200          ASR    R0         ;ANY DEVICE ACTIVE?
421 001200* 103404          BCS    3$        ;YES GO & INITIATE THE DEVICE DEC/X MODULE.
422 001202* 001454          BEQ    SCAN      ;ALL DONE, GO AND SCAN.
423 001204* 062701 000010          ADD     #10,R1    ;UPDATE CSR ADDRESS.
424 001210* 000772          BR      3$        ;CONTINUE.
425 001212* 005772 177016          TST    FIRST     ;IS IT FIRST PASS???
426 001216* 001013 000000          BNE    9$        ;NO, THEN DON'T INITIALIZE DEVICE
427 001220* 012711 040000          MOV     #BIT14,(R1) ;MASTER CLEAR FIRST TIME ONLY.
428 001224* 005061 000002          CLR    2(R1)     ;INITIALIZE THE UNIBUS CSRS.
429 001228* 005061 000004          CLR    4(R1)     ;INITIALIZE THE UNIBUS CSRS.
430 001232* 005061 000006          CLR    6(R1)     ;INITIALIZE THE UNIBUS CSRS.
431 001236* 005061 100000          MOV     #15,(R1)  ;INITIALIZE THE UNIBUS CSR'S.
432 001242* 012711 100000          TSTB   (R1)      ;IS RD I SET?
433 001250* 100415          BMI    12$       ;YES, THEN START DECX...
434 001252* 010067 176774          MOV     R0,SAR0  ;SAVE REGISTER R0...
435 001256* 101407 176772          MOV     R1,SAR1  ;SAVE REGISTER R1...
436 001260* 104407 000000          BREAKS,BEGIN    ;TEMPORARY RETURN TO MONITOR...
437 001266* 104407 000000          BREAKS,BEGIN    ;THEN CONTINUE AT NEXT INSTRUCTION.
438 001272* 016700 176754          MOV     SAR0,R0  ;RESTORE REGISTER R0...
439 001276* 016701 176752          MOV     #R1,R1  ;RESTORE REGISTER R1.
440 001304* 000761          BR      9$        ;WAIT FOR RD I TO SET...
441 001304* 052761 000020 12$:  BIS    #020,2(R1) ;SET IET.
442 001312* 105767 176744          TSTB   RCOLY      ;IS RECEIVE ONLY SET?
443 001316* 001403          BEQB   6$        ;NO, BRANCH TO NO.
444 001320* 001320 000020          BIS    #020,(R1) ;SET IET RECEIVE PA/CC I
445 001324* 000727          BR      2$        ;CONTINUE.
446 001326* 052711 000024          BIS    #024,(R1) ;SET IET XMITR PA/CC I.
447 001332* 000724          BR      2$        ;CONTINUE.
448
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457
;*****
; THIS ROUTINE SCANS ALL DEVICES END PASS FLAGS
; UNTIL ALL ACTIVE KMC11 DEVICES ARE FINISHED.
; UPDATES PASS COUNT AND LOOPS TILL 200 PASSES.
; ARE DONE CHECKS DATA AND PRINTS OUT DATA ERRORS.
; REPORTS END OF PASS.

```

```

458
459
460
461 001334* 012767 000003 176674 SCAN: MOV     #3,MASK ;SET BIT FOR ALL DEVICES.
462 001342* 012767 000010 176654          MOV     #10,DLV1 ;SET DELAY COUNT.
463 001350* 005067 176652          CLR    DLV2     ;CLEAR DELAY COUNT.
464 001354* 026767 176652 1$:  CMP     FLAG,XX11 ;IS DEVICE 1 ALL DONE?
465 001362* 001003          BNE    2$        ;NO, CHECK THE NEXT ONE.
466 001364* 042767 000001 176644          BIC    #BIT0,MASK ;CLEAR THE DEVICE BIT.
467 001372* 026767 176634 002656 2$:  CMP     FLAG,XX21 ;IS DEVICE #2 ALL DONE?
468 001400* 001003          BNE    3$        ;NO, GO AND WAIT.
469 001400* 042767 000002 176626          BIC    #BIT1,MASK ;CLEAR THE DEVICE BIT.
470 001410* 005767 176622          TST    MASK     ;ARE ALL DEVICES DONE?
471 001414* 001064          BNE    16$       ;NO, GO AND WAIT.
472 001416* 001064          MOV     #INTLNK+10,R1 ;R1 POINTS TO DEVICE CSR.
473 001418* 016700 004232          MOV     SELECT,R0 ;R0 CONTAINS BITS FOR ACTIVE DEVICES.
474 001426* 012703 005266          MOV     #BUFTAB,R3 ;R3=POINTER TO RECEIVER BUFFER.
475 001432* 006200          ASR    R0         ;IS ANY DEVICE ACTIVE?
476 001434* 103417          BCS    5$        ;YES, GO AND CHECK THE DATA.
477 001436* 001404          BEQ    6$        ;IS IT ALL DONE THEN BRANCH.
478 001440* 062701 000022          ADD     #2,R1    ;UPDATE R1 TO NEXT DEVICE CSR.
479 001444* 005723          TST   (R3)+     ;UPDATE R3 TO NEXT BUFFER.
480 001446* 000771          BR      4$        ;CONTINUE.
481 001450* 012767 177777 176556 6$:  MOV     #1,FIRST ;SET FIRST PASS FLAG.
482 001456* 012767 177777 176604          MOV     #1,FLAG  ;SET FLAG FOR MICRO-CODE LOADED.
483 001464* 104413 000000          ENDS$,BEGIN    ;SIGNAL END OF ITERATION.
484
485 001470* 000167 176632          JMP    LOOP     ;LOOP THE MODULE.
486
487
488
489
490
491
492
493
494 001474* 105767 176563          8$:  TSTB   XMOLY      ;IS IT XMIT ONLY???
495 001500* 001357          BNE    5$        ;YES, THEN DONT CHECK THE DATA...
496 001502* 011302          MOV     (R3),R2  ;R2=POINTS TO RECEIVER BUFFER.
497 001504* 012704          MOV     #R2,R4   ;R4=POINTS TO RECEIVER DATA.
498 001506* 012705 004266          MOV     #XBUF,R5  ;R5=POINTS TO GOOD DATA.
499 001512* 016767 176522 176546 10$:  MOV     #SIZE,TEMP ;SET THE BUFFER SIZE.
500 001520* 012514          CMPB   (R5),(R4) ;COMPARE DATA.
501 001522* 001434          BEQ    11$       ;GOOD COMPARE, NEXT CHAR.
502 001524* 011167 176350          MOV     (R1),CSRA ;LOAD CSRA.
503 001530* 010567 176346          MOV     #R5,SADR ;LOAD GOOD ADDRESS.
504 001534* 010467 176344          MOV     #R5,WASDR ;LOAD BAD ADDRESS.
505 001540* 111567 176342          MOV     (R5),ASB ;LOAD GOOD DATA.
506 001544* 111467 176340          MOV     (R4),AWAS ;LOAD BAD DATA.
507
508 001550* 104404 000000          DATERS,BEGIN    ;*****
509 001554* 122524          ;*****
510 001556* 005367 176504 11$:  CMPB   (R5)+,(R4)+ ;PCP BUFFER DATA POINTERS.
511 001562* 001356          DEC    TEMP     ;ALL DONE?
512 001564* 000725          BNE    10$      ;NO, DO THE NEXT.
513 001566*          BR      16$:   ;GO, AND CHECK REMAINING DEVICE.

```



```
514 001566* 104407 000000* BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
515 001572* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
516 ; ; ;THEN CONTINUE AT NEXT INSTRUCTION.
517 001576* 005367 176424 DEC DLY2 ;DECREMENT DELAY COUNT FOR #2.
518 001602* 001402 BEQ +6 ;
519 001604* 000167 177544 JMP IS ;WAIT FOR DEVICE TO COMPLETE.
520 001610* 005367 176410 DEC DLY1 ;DECREMENT DELAY COUNT FOR #1.
521 001614* 011402 BEQ +6 ;
522 001616* 000167 177532 JMP ;
523 001622* 016700 176410 MOV MASK,RO ;WAIT FOR DEVICE TO COMPLETE.
524 001626* 040067 176376 BIC RO,SELECT ;RO=HUNG DEVICE BITS.
525 001632* 006000 RDR RO ;DROP ANY HUNG DEVICE.
526 001634* 103004 BCC 17$ ;WAS DEV#1 HUNG?.
527 001636* 004367 000024 JSR R3,XERR ;BRANCH IF NO.
528 001642* 004232 CSRC1 ;TYPE ERROR MESSAGE & DROP.
529 ; ; ;POINTER TO DEV#1 CSR.
530 001644* 006000 RDR RO ;DEVICE NUMBER FOR TYPEOUT.
531 001650* 103004 BCC 18$ ;WAS DEV#2 HUNG?.
532 001652* 004367 000010 JSR R3,XERR ;BRANCH IF NOT.
533 001656* 042544 CSRC2 ;TYPE ERROR MESSAGE THEN DROP.
534 001662* 000167 176440 JMP LOOP ;POINTER TO DEV#2 CSR.
535 ; ; ;DEVICE NUMBER FOR TYPEOUT.
536 ; ; ;RESTART MODULE.
537 ; ; ;
538 ; ; ;
539 ; ; ;
540 ; ; ;
541 ; ; ;
542 ; ; ;
543 ; ; ;
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546 ; ; ;
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568 ; ; ;
569 ; ; ;

001666* 012302 000110 XERR: MOV (R3)+,R2 ;GET POINTER TO CSR.
001670* 012307 000060 MOV (R3)+,DEV ;GET DEVICE #.
001674* 052767 000060 BIS #60,DEV ;MAKE IT ASCII.
001702* 104403 000000 MSGNS,BEGIN,DROP1 ;ASCII MESSAGE CALL WITH COMMON HEADER
001712* 010167 176162 MOV (R2),R1 ;GET CSR ADDRESS.
001716* 011167 176160 MOV (R1),ACSR ;SAVE CONTENTS OF SEL0.
001722* 016167 000002 MOV 2(R1),ASTAT ;SAVE CONTENTS OF SEL2.
001730* 016167 000004 MOV 4(R1),DLY1 ;SAVE CONTENTS OF SEL4.
001736* 016167 000006 MOV 6(R1),DLY2 ;SAVE CONTENTS OF SEL6.
001744* 016267 000002 MOV 2(R2),ESAV1 ;SAVE END PASS FLAG.
001752* 011267 000272 MOV (R2),ESAV2 ;SAVE RECEIVE BUFFER OFFSET.
001756* 012767 000010 MOV 10(R2),ESAV3 ;SAVE RECIV/AMTR COUNTERS.
001764* 012767 000011 MOV #1,ERRTYP ;NO INTERRUPT
*****
001772* 104405 000000* 002256* HRDRS,BEGIN,ETABLE ;DUMP KMC CSR'S AND STATUS FLAGS
*****
020000* 005011 CLR (R1) ;SHUT OFF HUNG KMC11.
020002* 000203 RTS R3
DEV: ;WORD 0
DROP1: ;DEV
XDROP2 -1
```

```
570 002016* 002110* SOFT1: SOFT11
571 002020* 177777* -1
572 002022* 002171* SOFT2: SOFT21
573 002024* 177777* -1
574 ; ; ;
575 ; ; ;
576 ; ; ;
577 ; ; ;
578 002026* 020045 046513 030503 XDROP1: .ASCIZ /% KMC11 DEVICE # /
579 002034* 020061 042504 044526
580 002042* 042503 021440 000040
581 ; ; ;
582 002050* 051511 044040 047125 XDROP2: .ASCIZ /IS HUNG AND HAS BEEN DROPPED..%/
583 002056* 020107 047101 020104
584 002064* 040510 020123 042502
585 002072* 047105 042040 047522
586 002100* 050120 042105 027056
587 002106* 000045
588 ; ; ;
589 002110* 020045 051105 047522 SOFT11: .ASCIZ /% ERROR IN SETTING UP SWITCH REGISTER & RESTART /
590 002116* 020122 047111 051440
591 002124* 052105 044524 043516
592 002132* 052440 020120 053523
593 002140* 052411 044103 051040
594 002146* 043805 051511 042524
595 002154* 020122 020045 042522
596 002162* 052123 051101 020124
597 002170* 000
598 ; ; ;
599 002171* 045 042440 051122 SOFT21: .ASCIZ /% ERROR IN LOADING MICRO-CODE WILL RETRY...%/
600 002176* 051117 044440 020116
601 002204* 047514 042101 047111
602 002212* 020107 044515 051103
603 002220* 026517 047503 042504
604 002236* 053440 046111 020114
605 002234* 042522 051124 027131
606 002242* 027056 000045
607 ; ; ;
608 ; ; ;
609 ; ; ;
610 ; ; ;
611 002246* 000000 ESAV1: .WORD 0
612 002252* 000000 ESAV2: .WORD 0
613 002252* 000000 ESAV3: .WORD 0
614 002254* 000000 ESAV4: .WORD 0
615 ; ; ;
616 ; ; ;
617 ; ; ;
618 002256* 000224* ETABLE: DLY1
619 002260* 000226* DLY2
620 002262* 002246* ESAV1
621 002264* 002250* ESAV2
622 002266* 002252* ESAV3
623 002270* 177777* -1
624 002272* 002246* FTABLE: ESAV1
625 002274* 002250* ESAV2
```

626 002276 002252
628 002300 002254
629 002302 177777
630
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640 002304 010577 005046
641 002310 062767 000002 005040
642 002316 022767 007316 005032
643 002324 003003
644 002326 012767 007276 005022
645 002334 012605
646
647 002336 000004 000000 002344
648
649 002344 017705 005010
650 002350 062767 000002 005002
651 002356 022767 007316 004774
652 002362 003003
653 002366 012767 007276 004764
654 002374 016501 000004
655 002400 032711 000007
656 002404 011410
657 002406 032711 000004
658 002411 001033
659 002414 044403 000000 002554
660 002422 104400 000000
661 002426 016504 000010
662 002432 011467 175606
663 002436 042767 001510
664 002442 011467 175600 000004
665 002450 016761 175574 000006
666 002456 056761 175556 000006
667 002464 052765 000002 000006
668
669 002472 142711 000220
670
671 002476 104400 000000
672 002502 012767 004266 175534
673 002510 004767 001436
674 002514 016761 175526 000004
675 002516 056761 175502 000006
676 002530 056761 175506 000006
677 002536 052765 000001 000006
678
679 002544 142711 000200
680 002550 104400 000000
681 002554 002560

```
ESAV3  
ESAV4  
-1  
*****  
: INPUT INTERRUPT SERVICE ROUTINE  
: THIS ROUTINE SERVES THE IN INTERRUPT  
: FROM KMC11 BY LOADING REQUESTED XMTR  
: OR RECEIVE BA/CC I  
: *****  
INISR: MOV R5,INQIN ;SAVE LINK POINTER IN QUEUE.  
ADD #2,INQIN ;UPDATE THE QUEUE POINTER.  
CMP #PIRINQ+20,INQIN ;IS IT END OF QUEUE?  
BGT IS ;NO, CONTINUE  
MOV #PIRINQ,INQIN ;RESET QUEUE POINTER.  
MOV (SP)+,R5 ;RESTORE R5, I.F POP THE STACK.  
1$: -----  
PIRQS,BEGIN,2$ ; QUEUE UP TO CONTINUE AT 2$ AND RTI  
-----  
MOV #INGOUT,R5 ;RESTORE THE LINK POINTER.  
ADD #2,INGOUT ;UPDATE THE QUEUE POINTER.  
CMP #PIRINQ+20,INGOUT ;IS IT END OF QUEUE?  
BGT IS ;NO, CONTINUE  
MOV #PIRINQ,INGOUT ;RESET THE QUEUE POINTER.  
3$: MOV 4(R5),R1 ;LOAD CSR ADDRESS.  
BIT #7,(R1) ;RECEIVE BA/CC I?  
BEQ RCV ;BRANCH IF YES.  
BIT #4,(R1) ;XMTR BA/CC I?  
BNE XMTR ;BRANCH IF YES.  
MSGNS,BEGIN,ILINT ;ASCII MESSAGE CALL WITH COMMON HEADER  
EXIT$;BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.  
RECVR: MOV 10(R5),R4 ;GET RECEIVE BUFFER POINTER.  
MOV (R4),VA ;VA=RECEIVE BUFFER VIRTUAL ADDRESS..  
JSR PC,EARITS ;GET PHYSICAL ADDRESS.  
MOV EA,4(R1) ;SEL4=PHYSICAL ADDRESS.  
MOV EA,6(R1) ;SEL6=EXTENDED BITS OF ADDRESS.  
RIS #SIZE,6(R1) ;LOAD RECEIVE CHARACTER COUNT.  
BIS #BIT1,6(R5) ;SET BIT1 IN END PASS FLAG FOR  
;BA/CC I LOADED  
RIS #BIT7,BIT4,(R1) ;CLEAR RD1 TO REQUEST SERVICE FROM  
;KMC11. CLEAR IE1  
EXIT$,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.  
XMTR$: MOV #XBUF,VA ;LOAD XBUFFER VIRTUAL ADDRESS.  
JSR PC,EARITS ;GET EXTENDED PHYSICAL ADDRESS.  
MOV PA,4(R1) ;LOAD PHYSICAL XMTR BUFFER.  
MOV EA,6(R1) ;LOAD EXTENDED ADDRESS BITS XMTR BUFFER.  
RIS #SIZE,6(R1) ;LOAD XMTR CHARACTER COUNT.  
BIS #BIT0,6(R5) ;SET BIT0 IN END PASS FLAG FOR  
;XMTR BA/CC I LOADED  
1$: BICR #BIT7,(R1) ;CLEAR RD1 TO INDICATE START OF OPERATION  
EXIT$,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.  
ILINT: WES2
```

682 002556 177777
683 002560 044445 050116 052125
684 002566 044440 052116 051105
685 002574 052522 052120 053440
686 002602 052711 020110 047516
687 002610 051040 050505 042525
688 002616 052123 051440 052105
689 002624 052440 020520 020441
690 002632 000045
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703 002634 010577 004522
704 002640 062767 000002 004514
705 002646 022767 007336 004506
706 002654 003003
707 002656 012767 007316 004476
708 002664 012605
709
710 002666 000004 000000 002674
711
712 002674 017705 004464
713 002700 062767 000002 004456
714 002706 022767 007336 004450
715 002714 003003
716 002716 012767 007316 004440
717 002724 011501 000002 000002
718 002726 032761 000002 000002
719 002734 011410 000010 000002
720 002736 032761 000010 000002
721 002744 001403 000000 003156
722 002746 104403 000100 000002
723 002754 032761 000000 003162
724 002762 001403 000040 000002
725 002764 104403 000000 003152
726 002772 032761 000000 000002
727 003000 001403
728 003002 104403 000000 003152
729 003010 010167 175064
730 003014 016167 000004 175060
731 003022 051677 000006 175054
732 003030 012767 000001 175050
733
734 003036 104405 000000 000000
735
736
737 003044 142761 000352 000002

```
MES2: -1  
;ASCIZ /*INPUT INTERRUPT WITH NO REQUEST SET UP!!!!*/  
-----  
; EVEN  
: *****  
: OUTPUT INTERRUPT SERVICE ROUTINE  
: THIS ROUTINE SERVES THE OUT INTERRUPT  
: FROM KMC11 BY 1 REPORTING ERROR  
: 2 ACCEPTING RECEIVE OR XMTR DONE  
: *****  
OUTISR: MOV R5,OUTQIN ;SAVE LINK POINTER IN QUEUE.  
ADD #2,OUTQIN ;UPDATE THE QUEUE POINTER.  
CMP #PIROUTQ+20,OUTQIN ;IS IT END OF QUEUE?  
BGT IS ;NO, CONTINUE  
MOV #PIROUTQ,OUTQIN ;RESET THE QUEUE POINTER.  
MOV (SP)+,R5 ;POP THE STACK POINTER  
1$: -----  
PIRQS,BEGIN,2$ ; QUEUE UP TO CONTINUE AT 2$ AND RTI  
-----  
MOV #OUTQOUT,R5 ;RESTORE THE LINK POINTER FROM QUEUE.  
ADD #2,OUTQOUT ;UPDATE THE QUEUE POINTER.  
CMP #PIROUTQ+20,OUTQOUT ;IS IT END OF QUEUE?  
BGT IS ;NO, CONTINUE  
MOV #PIROUTQ,OUTQOUT ;RESET THE QUEUE POINTER.  
3$: MOV (R5),R1 ;LOAD CSR ADDRESS.  
BIT #BIT1,2(R1) ;IS CONTROL/O ERROR REPORT?  
BEQ RCV ;NO, THEN CHECK FOR RCV OR XMTR.  
BIT #BIT3,2(R1) ;IS IT SOFT ERROR.  
BEQ RCV ;NO, THEN CHECK REMAINING...  
MSGNS,BEGIN,SFT1 ;ASCII MESSAGE CALL WITH COMMON HEADER  
BIT #BIT6,2(R1) ;IS IT DATA ERROR  
BEQ RCV ;NO, CHECK THE REMAINING...  
MSGNS,BEGIN,DTER1 ;ASCII MESSAGE CALL WITH COMMON HEADER  
BIT #BIT5,2(R1) ;IS IT NON EX MEMORY ERROR.  
BEQ RCV ;NO THEN MUST BE DATA ERROR.  
MSGNS,BEGIN,NXMMRY ;ASCII MESSAGE CALL WITH COMMON HEADER  
5$: MOV R1,CSR ;LOAD DEVICE CSR ADDRESS.  
MOV 4(R1),ACSR ;LOAD CONTENTS OF DEVICE CSR.  
MOV 6(R1),ASTAT ;LOAD ERROR BITS.  
MOV #1,ERRTYP ;DATA ERROR  
;*****  
;HDRS$;BEGIN NULL ;A CNTL/O RECEIVED  
;*****  
BICR #352,2(R1) ;ASTAT= ERROR BITS.  
;CLEAR RD0, NON EX MEM., CNTL/O.
```

```

738 003052* 104400 000000*          EXITS,BEGIN          ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
739                                     ;
740 003056* 032761 000004 000002 4$: BIT #BIT2,2(R1)          ;IS IT XMTR DONE?
741 003064* 001411                                     ;NO,RECEIVEISERVE IT.
742                                     ;
743 003066* 052765 000004 000002     BIS #BIT2,2(R5)          ;SET XMTR DOWF BIT IN END PASS FLAG.
744 003074* 105767 175163     TSTB XNOLY,          ;IS IT XMIT ONLY???
745 003100* 001006     BNE 7$          ;CONTINUE.
746 003102* 142711     BICB #4,(R1)          ;SET FOR RECEIVE BA/CC I...
747 003106* 000403     BR 7$          ;CONTINUE.
748 003110* 052765 000010 000002 6$: BIS #BIT3,2(R5)          ;SET RECEIVE DONE BIT IN ENDPASS FLAG.
749 003116* 026765 175110     CMP FLAG,2(R5)          ;ALL DONE?
750 003124* 001005     BNE 8$          ;NO,CONTINUE.
751 003136* 142761 000020 000002     BICB #BIT4,2(R1)          ;CLEAR IO.
752 003144* 142711 000020     BICB #BIT4,(R1)          ;CLEAR THE 'IEI.
753 003140* 142761 000205 000002 8$: BICB #BIT7|BIT2|BIT0,2(R1) ;CLEAR RDO,XMTR OR RCV DONE, BA/CC 0...
754 003146* 104400 000000*          EXITS,BEGIN          ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
755 003152* 001666     NXMMRV: MES1 -1
756 003154* 177777     SFT1: MES4 -1
757 003156* 003303     DTER1: MES5 -1
758 003160* 177777     -1
759 003162* 077777     -1
760 003164* 177777     -1
761 003166* 020045 047516 020116 MES1: .ASCII /% NON EXISTENT MEMORY ADDRESS ERROR %/
762 003174* 054105 051511 042504
763 003210* 051119 020131 042101
764 003216* 051104 051505 020123
765 003224* 051105 047522 020122
766 003234* 026040 041517 052503 .ASCIZ / OCCURED WHILE DOING NPR*S..... %/
767 003242* 042522 020104 044127
768 003250* 046111 020105 047504
769 003264* 023522 027123 027056
770 003272* 027056 027056 027056
771 003300* 027440 051840 043117 MES4: .ASCIZ /% SOFT ERROR %/
772 003310* 020124 051105 047522
773 003316* 020122 000045
774 003322* 020045 040504 MES5: .ASCIZ /% DATA ERROR ON TRANSMIT %/
775 003336* 047440 051122
776 003344* 047101 020116 051124
777 003352* 027440 000 052111
778 003356* 003356*          .EVEN
779                                     ;*****
780                                     ;* SURROUTINE TO LOAD MICRO-CODE INTO CRAM.
781                                     ;* AND THE PARAMETERS INTO SCRATCH PAD.
782                                     ;* R1 CONTAINS THE CSR AT THE TIME OF ENTPY.
783                                     ;*****
784 WCRAM: CLR R0          ;R0=POINTS TC CRAM ADDRESS.
785
786
787
788
789
790
791
792
793 003356* 005000

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```

794 003360* 012702 007372*          MOV #KMAAMC,R2          ;R2 POINTS TO MICRO-CODE.
795 003364* 005011     CLR (R1)          ;CLEAR SEL0.
796 003366* 010061 000004     MOV R0,4(R1)          ;LOAD THE CRAM ADDRESS...
797 003372* 012261 000006     MOV (R2)+,6(R1)          ;LOAD WORD TC BE WRITTEN...
798 003376* 012711 002000     MOV #BIT10,(R1)          ;SET ROM0.
799 003402* 012711 022000     MOV #BIT13|BIT10,(R1) ;WRITE IT!
800 003406* 005200     INC R0          ;INCREMENT CRAM ADDRESS.
801 003410* 022700 002000     CMP #2000,P0          ;OVER FLOW?
802 003414* 022700 000000     BLT 2$          ;YES,REPORT AND RETURN.
803 003416* 022719 000000     CMP #0,(R2)          ;IS IT END OF MICRO-CODE?
804 003422* 001360     BNE IS          ;NO,CONTINUE LOADING.
805 003424* 012705 000011     MOV #1,R5          ;R5=THE SCRATCH PAD ADDRESS
806 003430* 012702 000262*     MOV #COLY,R2          ;SET THE PARAMETER POINTER.
807 003434* 005011     CLR (R1)          ;CLEAR SEL0.
808 003436* 042767 000077 000014     BIC #77,4$          ;CLEAR THE ADDRESS IN INSTRUCTION..
809 003444* 050567 000010     BIS #5,4$          ;SET SCRATCH PAD ADDRESS
810 003450* 112621 000004     MOV (R2)+,4(R1)          ;LOAD SEL4 WITH DATA.
811 003454* 004367 000446     JSR R3,ROMCLK          ;CLOCK THE INSTRUCTION IN
812 003460* 123100          ; THIS LOCATION.
813 003462* 005205     INC R5          ;INCREMENT SCRATCH PAD ADDRESS.
814 003464* 027705 000015     CMP #15,R5          ;IS IT DONE?
815 003470* 003361     BGT 3$          ;BRANCH IF NOT DONE.
816 003472* 005011     CLR (R1)          ;CLEAR SEL0.
817 003474* 000207     RTS PC          ;RETURN
818
819 003476* 104403 000000* 003506* 2$: MSGN$,BEGIN,CRM0FW ;ASCII MESSAGE CALL WITH COMMON HEADER
820 003504* 000772     BR 5$          ;RETURN.
821
822 CRM0FW: MES3 -1
823 003510* 177777     -1
824 003512* 020045 044515 051103 MES3: .ASCIZ /% MICRO-CODE OVER FLOWS CRAM %/
825 003520* 026517 047503 042504
826 003526* 047440 053517 020122
827 003534* 046106 053517 020123
828 003542* 051103 046501 022440
829 003550* 000 022440
830 003552* 003552*          .EVEN
831                                     ;*****
832                                     ;* THIS ROUTINE WRITES GOOD DATA BUFFER
833                                     ;* INTO LOWER HALF OF THE MAIN MEMORY...
834                                     ;* R1 CONTAINS THE DEVICE CSR ADDRESS.
835                                     ;*****
836 WMEMRV: JSR R3,ROMCLK          ;CLOCK INSTRUCTION.
837 010000          ;LOAD MAR(0)?
838 JSR R3,ROMCLK          ;CLOCK INSTRUCTION.
839 004002          ;SET POINTER IN KMC11 MEMORY.
840 MOV #XBUF,R0          ;SET POINTER TO DATA.
841 MOV #100,R2          ;SET THE COUNT.
842 CLR 4(R1)
843 1$: MOVB (R0)+,4(R1)          ;BSEL4<--GOOD DATA.
844 JSR R3,ROMCLK
845 003552* 004367 000350
846 003556* 010000
847 003560* 004367 000342
848 003564* 004002
849 003566* 012700 004266*
850 003572* 012702 001000
851 003576* 005061 000004
852 003602* 112061 000004
853 003606* 004367 000314

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850 003612 136500
851 003614 005302
852 003616 005011
853 003618 005011
854 003622 000207
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864
865
866 003624 005067 174406
867 003626 017700 007372
868 003630 005002
869 003634 005011
870 003636 005011
871 003640 010261 000004
872 003644 005011 002000
873 003650 011005
874 003652 016104 000006
875 003656 020504
876 003660 017403
877 003662 005167 174350
878 003666 000515
879 003670 005720
880 003674 022710
881 003700 001403 000000
882 003702 022710 002000
883 003706 022953
884
885 003710 000011
886 003712 012705 000011
887 003714 012704 000262
888 003716 052911
889 003722 042767 000077 000014
890 003730 050567 000010
891 003734 052061 000004
892 003736 005067 000162
893 003744 040600
894 003746 004367 000154
895 003752 061224 000004
896 003754 005167
897 003760 001403
898 003762 005167 174250
899 003766 005167
900 003770 005167
901 003772 022705 000015
902 003776 003350
903 004000 017403 004266
904 004004 017403 001000
905 004012 016702 174224

```
136500 ;LOAD MEMORY AND INCREMENT ADDRESS.  
DEC R2 ;COUNT BY ONE  
BNE R2 ;BRANCH IF NOT DONF..  
CLR (R1) ;CLEAR THE CSR.  
RTS PC ;RETURN TO MAINLAND.  
*****  
; VERIFY THE MICRO-CODE  
; THIS ROUTINE VERIFIES THE MICRO-CODE  
; LOADED, AND CHECKS NBR RATE PARAMETER.  
; AND THE MAIN MEMORY CONTENTS.....  
; R1 CONTAINS THE ADDRESS OF DEVICE CSR.  
*****  
VERIFY: CLR MASK ;CLEAR THE ERROR FLAG.  
MOV #KMAAMC,R0 ;R0 POINTS TO SOFTWARE MICRO-CODE.  
CLR R2 ;R2 CONTAINS CRAM ADDRESS BITE 0-7  
1$: CLR (R1) ;CLEAR THE MAINTANENCE REGISTER..  
MOV #24(R1) ;SET THE CRAM ADDRESS...  
MOV #8(R1) ;SET ROMO  
MOV (R0),R5 ;PUT "EXPECTED" IN R5.  
MOV (R1),R4 ;PUT "FOUND" IN R4.  
CMP R5,R4 ;COMPARE  
REQ #3 ;COMPARE IF D.K.  
COM MASK ;SET ERROR FLAG.  
BR 4$ ;RETURN  
3$: TST (R0)+ ;BUMP MICRO-CODE POINTER.  
TST R2 ;BUMP CRAM ADDRESS.  
CMP #0,(R0) ;IS IT DONE?  
REQ #5 ;YES, GO AND CHECK THE MEMORY...  
CLR #000,R2 ;CLEAR SEL4  
BCE 1$ ;NO,CONTINUE.....  
5$: MOV #11,R5 ;R5=SPAD ADDRESS.  
MOV #R6(R1),R4 ;SET THE PARAMETER ADDRESS.  
6$: CLR SEL0 ;CLEAR SEL0  
BIC #77,R5 ;CLEAR THE ADDRESS FIELD...  
BIS #75,R5 ;SET SCRATCH PAD ADDRESS IN INSTR.  
JSR R3,ROMCLK ;CLOCK THE INSTRUCTION.  
7$: MOV SPAD(R5)->PERG. ;MOVE SPAD(R5)->PERG.  
JSR R3,ROMCLK ;CLOCK THE INSTRUCTION IN NEXT LOCATION.  
CLR #4(R1) ;MOVE PERG,OUT<CSR4>.  
CMPB (R4)+,4(R1) ;COMPARE  
REQ #8 ;GOOD, DO THE NEXT.  
COM MASK ;SET ERROR FLAG.  
BR 8$ ;RETURN  
8$: INC R5 ;UPDATE R5 POINTING TO NEXT SPAD.  
CMP #15,R5 ;IS IT DONE?  
BGT #6$ ;BRANCH IF NOT DONE.  
9$: MOV #TRUF,R0 ;SET THE DATA ADDRESS.  
MOV #1000,FLAG ;SET THE BUFFER POINTER.  
MOV XSIZE,R2 ;SET THE COUNT...
```

906 004016 042767 000377 000026
907 004034 052767 000033 000026
908 004036 156767 174225 000019
909 004040 156767 174225 000012
910 004046 004367 000054
911 004052 010000 000046
912 004054 004000 000040
913 004060 004000 000032
914 004062 004367 000004
915 004066 004367 000032
916 004070 005167 000004
917 004074 061224
918 004076 122061 000004
919 004102 001403
920 004104 005167 174126
921 004110 000404
922 004112 005267 174152
923 004116 005302
924 004120 001336
925 004122 005011
926 004124 000207
927
928 004126 112761 001000 000001
929 004134 012361 000006
930 004140 052711 001400
931 004144 042711 003400
932 004150 000203
933
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943
944 004152 104415 000000 000244
945 004160 000367 174064
946 004164 006167 174060
947 004170 006167 174054
948 004174 042767 003776 174046
949 004202 000207
950
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959 004204 005067 174056
960 004210 000467 174052
961 004214 005303

```
12$: BIC #377,15$ ;CLEAR THE ADDRESS FIELD.  
BIC #16,16$ ;CLEAR THE ADDRESS FIELD.  
BISB FLAG,15$ ;ADD ADDRESS TO INSTRUCTION.  
BISB FLAG,1,18$ ;ADD ADDRESS TO INSTRUCTION.  
15$: JSR R3,ROMCLK ;LOAD MAR_LD.  
18$: JSR R3,ROMCLK ;LOAD MAR_HI.  
JSR R3,ROMCLK ;BREG<--MEM.  
JSR R3,ROMCLK ;BSEL4<--BREG.  
CMPB (R0)+,4(R1) ;COMPARE THE DATA.  
REQ #21 ;BRANCH IF GOOD  
COM MASK ;ELSE,SET THE ERROR FLAG.  
BR 4$ ;RETURN  
21$: INC FLAG ;INCREMENT THE ADDRESS...  
DEC R2 ;DONE?  
BNE 12$ ;NO,CHECK THE NEXT.  
4$: CLR (R1) ;CLEAR SEL0.  
RTS PC ;RETURN.  
*****  
; SUBROUTINE EABITS:- GETS PHYSICAL 18 BITS ADDRESS FOR  
; 16 BITS VIRTUAL ADDRESS.  
; RETURNS: ADDRESS IN P1::ADDRESS<0:15>  
; EA::ADDRESS<16:17>  
*****  
EABITS: GETPAS,BEGIN, VA ;GET PHYSICAL ADDRESS FROM 16-BIT VA  
SWAB EA ;BITS<4:5>-->BITS<12:13>  
ROL EA ;NOW BITS<4:13>  
ROL EA ;NOW BITS<15:14>  
BIC #3776,EA ;CLEAR THE REST..  
RTS PC ;RETURN..  
*****  
; MULTIPLIES #'S IN R3 AND R4 AND RETURNS RESULT IN  
; TEMP.....  
*****  
MULTPLY: CLR TEMP ;CLEAR THE RESULT...  
1$: ADD R4,TEMP ;MULTIPLY BY ONE...  
DEC R3 ;COUNT BY ONE...
```

```

962 004216 001374
963 004220 000205
964
965
966
967
968
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970
971 004222
972 004225 004567 176056
973 004226 004567 176402
974 004232 000000
975 004233 000000
976 004236 005272
977 004240 000000
978 004242 000000
979
980 004244 004567 176034
981 004250 004567 176360
982 004254 000000
983 004256 000000
984 004260 000000
985 004262 000000
986 004264 000000
987
988
989
990
991
992
993
994 004266 000400 001402 002404
995 004274 003406 004410 012
996 004301 0913 006414 007416
997 004306 010420 011422 012424
998 004314 013426 014430 015432
999 004322 016434 017436 018438
1000 004327 041 022444
1001 004334 023446 024450 025452
1002 004342 026454 027456 030460
1003 004350 031460 032464 033466
1004 004356 034466 036472
1005 004362 037075 040077 041101
1006 004370 042103 043105 044107
1007 004376 045111 046113 047115
1008 004384 050117 051122 052123
1009 004412 053125 054127
1010 004416 055131 056133 057135
1011 004424 056052 041101 042103
1012 004428 058105 045111 046113
1013 004440 046113 047115 050117
1014 004446 051121
1015 004450 051123 053125 054127
1016 004456 051123 030533 077535
1017 004464 100600 202

```

```

;BNE RTS 1S ;NOT DONE THEN CONTINUE,
;RTS 1S ;RETURN...
;*****
;LINK TABLE TO INTERRUPT SERVICE ROUTINE.
;*****
INTLNK:
JSR R5,INISR ;INPUT INTERRUPT SERVICE ROUTINE.
JSR R5,OUISR ;OUTPUT INTERRUPT SERVICE ROUTINE.
CSR1: .WORD 0 ;CSR ADDRESS FOR DEV#1.
XX1: .WORD 0 ;END PASS FLAG FOR DEV#1.
RBUF1: .WORD 0 ;RECEIVE BUFFER POINTER FOR DEV #1.
XX12: .WORD 0 ;RECEIVE BUFFER POINTER FOR DEV #1.
RBUF2: .WORD 0 ;RECEIVE BUFFER POINTER FOR DEV #2.
XX22: .WORD 0 ;RECEIVE BUFFER POINTER FOR DEV #2.
RBUF1: .WORD 0 ;RECEIVE BUFFER POINTER FOR DEV #1.
RBUF2: .WORD 0 ;RECEIVE BUFFER POINTER FOR DEV #2.
CSR22: .WORD 0 ;CSR ADDRESS FOR DEV #2.
XX21: .WORD 0 ;END PASS FLAG FOR DEV #2.
RBUF2: .WORD 0 ;RECEIVE BUFFER POINTER FOR DEV #2.
XX22: .WORD 0 ;RECEIVE BUFFER POINTER FOR DEV #2.
RBUF1: .WORD 0 ;RECEIVE BUFFER POINTER FOR DEV #1.
RBUF2: .WORD 0 ;RECEIVE BUFFER POINTER FOR DEV #2.

```

```

;*****
;BUFFERS & QUEUES.
;*****
XBUF: .ASCII <000><001><002><003><004><005><006><007><010><011><012>
.ASCII <013><014><015><016><017><020><021><022><023><024><025>
.ASCII <026><027><030><031><032><033><034><035><036><037><040>
.ASCII ~!#$%&'()*+,-./0123456789:<
.ASCII /=>?@ABCDEFGHIJKLMNPOQRSTUVWXYZ/
.ASCII /YZ[\]^_`ABCDEFGHIJKLMNPOQR/
.ASCII /SXUVWXYZ[1]/<177><200><201><202>

```

```

1018 004467 203 102604 103606 .ASCII <203><204><205><206><207><210><211><212><213><214><215>
1019 004474 104610 105612 106614 .ASCII <216><217><220><221><222><223><224><225><226><227><230>
1020 004502 107616 108618 109620 .ASCII <231><232><233><234><235><236><237><240><241><242><243>
1021 004510 112624 113626 230 .ASCII <244><245><246><247><250><251><252><253><254><255><256>
1022 004515 231 115632 116634 .ASCII <257><260><261><262><263><264><265><266><267><270><271>
1023 004523 117636 120640 124650 .ASCII <272><273><274><275><276><277><300><301><302><303><304>
1024 004530 122644 123646 124650 .ASCII <305><306><307><310><311><312><313><314><315><316><317>
1025 004536 125652 126654 256 .ASCII <320><321><322><323><324><325><326><327><330><331><332>
1026 004543 128656 130660 131662 .ASCII <333><334><335><336><337><340><341><342><343><344><345>
1027 004550 132664 133666 134668 .ASCII <346><347><350><351><352><353><354><355><356><357><360>
1028 004556 135672 136674 137676 .ASCII <361><362><363><364><365><366><367><370><371><372><373>
1029 004564 140700 141702 304 .ASCII <374><375><376><377><377>
1030 004571 305 143706 144710 ;
1031 004576 145712 146714 147716 ;
1032 004604 150720 151722 152724 .EVEN
1033 004612 153726 154730 .ASCII <374><375><376><377><377>
1034 004617 333 156734 157736
1035 004624 160740 161742 162744 .ASCII <374><375><376><377><377>
1036 004632 163746 164750 165752
1037 004640 166754 167756 360
1038 004645 361 171762 172764 .ASCII <374><375><376><377><377>
1039 004652 173766 174770 175772
1040 004660 176774 177776 377
1041 004665 000400
1042
1043 005266
1044
1045 005266 005272 BUFTAB: RBUF1 ;BUFFER POINTER FOR DEV #1.
1046 005270 005274 RBUF2 ;BUFFER POINTER FOR DEV #2.
1047
1048
1049
1050
1051 005272 TABLE OF RECEIVE BUFFERS.
1052 005272 005276 RBUF1: RBUF11 ;RECEIVE BUFFERS FOR DEV #1.
1053
1054 RBUF2: RBUF21 ;RECEIVE BUFFERS FOR DEV #2.
1055 005274 006276
1056
1057
1058
1059 005276 001000 RBUF11: .BLKB 1000 ;RECEIVE BUFFER 11.
1060
1061
1062
1063 006276 001000 RBUF21: .BLKB 1000 ;RECEIVE BUFFER 21.
1064 007276
1065
1066
1067
1068
1069
1070 007276 000010 BASE1:
1071 007316 000010 PTRINQ: .BLKW 10 ;INPUT INTERRUPT QUEUE.
1072 007336 000010 PTROUTQ: .BLKW 10 ;OUTPUT INTERRUPT QUEUE.
1073 007356 000000 REQQ: .BLKW 10
INQIN: .WORD 0 ;INPUT QUEUE POINTER.

```

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1074 007360 000000
1075 007362 000000
1076 007364 000000
1077 007366 000000
1078 007370 000000
1079 007372
1080
1081
1082
1083
1084
1085
1086
1087
1088
1089
1090
1091
1092
1093
1094
1095
1096
1097
1098
1099
1100
1101
1102
1103
1104
1105 007372
1106 007374
1107 007376
1108 007400
1109 007402
1110 007404
1111 007406
1112 007410
1113 007412
1114 007414
1115 007416
1116 007420
1117 007422
1118 007424
1119 007426
1120 007430
1121 007432
1122 007434
1123 007436
1124 007440
1125 007442
1126 007444
1127 007446
1128 007450
1129 007452

```

```

INQOUT: .WORD 0 ;INPUT QUEUE POINTER.
OUTQIN: .WORD 0 ;OUTPUT QUEUE POINTER.
OUTQOUT: .WORD 0 ;OUTPUT QUEUE POINTER.
REGQI: .WORD 0 ;REGISTER QUEUES.
REGQO: .WORD 0 ;REGISTER QUEUES.
MICRCD:
;*****
;DEC/X11 MICRO-CODE FOR KMC11 IT WORKS AS FOLLOWS:
;1) INTERRUPT 11 PROGRAM WHEN
;   A. IET & RDI SET - INTERRUPT AT LOC. XX0 FOR REQUEST OF
;   RECEIVE OR XMIT BA/CC I.
;   B. IEO & RDO SET - INTERRUPT AT LOC. XX4 FOR REPORTING
;   ERRORS (BY CNTL70) OR REPORTING THE JOB DONE
;2) WHEN ROI CLEARED BY THE 11 PROGRAM IT GOES AND
;   SERVES THE REQUEST I.E. EITHER RECEIVE OR XMIT.
;   A RECEIVE OPERATION IS TO RECEIVE A BUFFER FROM
;   KMC11 TO PDP11.
;   B. TRANSMIT OPERATION IS TO TRANSMIT A BUFFER FROM
;   PDP11 TO KMC11.
;3) IN NORMAL CASE (BOTH RECEIVE & XMIT) XMIT HAS TO BE DONE
;   FIRST THEN RECEIVE.
;4) IT CONTROLS NFR RATE BY INTERNAL SOFTWARE CLOCK & NFR/BR
;   RATE IS CONTROLLED BY CHARACTER COUNT.
;5) IN CASE OF XMIT OPERATION DATA CHECKING IS DONE
;   INSIDE KMC11.
;*****
;KMAANC: MOVE # 0,BREG ;CLEAR B REGISTER
;           MOVE # 0,MLR ;MAR <0:7>:=0.
;           MOVE # 0,NFR ;MAR <8:9>:=0.
;           MOVE BREG,SPAD <0> ;CLEAR SPAD <0>.
;           MOVE BREG,SPAD <1> ;CLEAR SPAD <1>.
;           MOVE BREG,SPAD <2> ;CLEAR SPAD <2>.
;           MOVE BREG,SPAD <3> ;CLEAR SPAD <3>.
;           MOVE BREG,SPAD <4> ;CLEAR SPAD <4>.
;           MOVE BREG,SPAD <5> ;CLEAR SPAD <5>.
;           MOVE BREG,SPAD <6> ;CLEAR SPAD <6>.
;           MOVE BREG,SPAD <7> ;CLEAR SPAD <7>.
;           MOVE BREG,SPAD <10> ;CLEAR SPAD <10>.
;           MOVE BREG,SPAD <17> ;CLEAR SPAD <17>.
;           MOVE BREG,OUT1 <0> ;CLEAR BSEL0.
;           MOVE BREG,OUT1 <1> ;CLEAR BSEL1.
;           MOVE BREG,OUT1 <3> ;CLEAR BSEL3.
;           MOVE BREG,OUT1 <4> ;CLEAR BSEL4.
;           MOVE BREG,OUT1 <5> ;CLEAR BSEL5.
;           MOVE BREG,OUT1 <10> ;CLEAR BSEL6.
;           MOVE BREG,OUT1 <17> ;CLEAR BSEL7.
;           MOVE BREG,OUT0 <1> ;
;           MOVE BREG,OUT0 <2> ;

```

```

1130 007454
1131 007456
1132 007460
1133 007462
1134 007464
1135 007466
1136 007470
1137 007472
1138 007474
1139 007476
1140 007500
1141 007502
1142 007504
1143 007506
1144 007510
1145 007512
1146 007514
1147 007516
1148 007520
1149 007522
1150 007524
1151 007526
1152 007530
1153 007530
1154 007534
1155 007536
1156 007536
1157 007540
1158 007542
1159 007544
1160 007546
1161 007550
1162 007552
1163 007554
1164 007556
1165 007560
1166
1167
1168
1169
1170
1171
1172
1173 007562
1174 007564
1175 007566
1176 007570
1177 007572
1178 007574
1179 007576
1180 007600
1181 007602
1182 007604
1183 007610
1184 007612
1185 007616

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;           MOVE BREG,OUT0 <3> ;
;           MOVE BREG,OUT0 <4> ;
;           MOVE BREG,OUT0 <5> ;
;           MOVE BREG,OUT0 <6> ;
;           MOVE BREG,OUT0 <7> ;
;           MOVE BREG,OUT0 <10> ;
;1$: MOVE # 0,MEM,MARINC ;CLEAR MEMORY LOCATION & INC MAR
;     SINCR SPAD <0> ;INCREMENT THE COUNT
;     SADC SPAD <1> ;ADD CARRY IN TO MSB.
;     SRR SPAD <1>,BREG ;IS IT DONE...
;     STRTS SRR ;SRR IF DONE AND START.
;           SRR ;ELSE, CONTINUE
;STRTS: MOVE # 200,BREG ;SET RD 1 BIT IN B REGISTER.
;         INP1 <CSR0>,SPAD <0> ;SAVE SEL0 IN SPAD <0>.
;         OR BREG,SPAD <0>,OUT1 <CSR0> ;SET RD 1 IN SEL0.
;MCDLP: MOVE INP1 <CSR2>,BREG ;IS RDO SET??
;         BBT MCDLP ;WAIT TILL IT CLEARS...
;         INP1 <CSR0>,BREG ;LOAD BREG WITH SEL0.
;         IS ;IF RD 1 SET, GO CHECK IET.
;         SRR PRCSBF ;IF CLEARED, PROCESS BUFFERS.
;         INTRPT ;IF ICI IS ALSO SET THEN GO INTERRUPT
;         SRR ;ELSE WAIT.
;INTRPT: MOVE # 0,BREG ;SET UP TO CLEAR CSRS.
;         MOVE BREG,OUT1 <CSR4> ;CLEAR BSEL4.
;         MOVE BREG,OUT1 <CSR5> ;CLEAR BSEL5.
;         MOVE BREG,OUT1 <CSR6> ;CLEAR BSEL6.
;         MOVE BREG,OUT1 <CSR7> ;CLEAR BSEL7.
;         MOVE # 200,BREG ;SET BR REQ. WITH VECTR=XX0.
;         OR SPAD <0>,BREG,OUT1 <CSR11> ;SET BR REQ. I.E. INTERRUPT AT XX0.
;1$: MOVE INP1 <CSR11>,BREG ;IS BR REQ BIT CLEARED.
;         BBT ;NO, WAIT OR SPIN ON IT.
;2$: MOVE INP1 <CSR0>,BREG ;IS IT SERVED??
;         BBT ;NO THEN WAIT...
;         SRR MCDLP ;RETURN TO LOOP.
;*****
; PROCESSES THE BUFFER REQUEST.
;*****
;PRCSBF: MOVE INP1 <CSR6>,SPAD <15> ;LOAD SPAD <15> WITH C.C LOW BYTE.
;         MOVE # 7,BREG ;LOAD MASKING BITS IN B REGISTER
;         INP1 <CSR7>,SPAD <16> ;GET HIGH BYTE OF CHARACTER COUNT.
;         AND BREG,SPAD <16>,SPAD <16> ;LOAD HIGH BYTE OF C.C. IN SPAD <16>.
;2$: MOVE # 7,BREG ;LOAD MASK IN B REG.
;         MOVE INP1 <CSR0>,SPAD <0> ;GET BSEL0.
;         AND BREG,SPAD <0>,BR,SP ;MASK OUT AND LOAD IN BREG & SPAD.
;         MEM,SPAD <4>
;         MOVE # 4,MEM ;LOAD EXPECTED IN MEM.
;         SIFEQ MEM,SPAD <0> ;XMIT BA/CC I LOADED!! SERVE IT!
;         MOVE # 0,MEM ;LOAD EXPECTED IN MEM.
;         SIFEQ MEM,SPAD <0> ;RCV BA/CC I LOADED!! SERVE IT!
;         MOVE SPAD <4>,MEM ;RESTORE MEMGRY LOCATION

```

```
1186 007620 *
1187 007622 *
1188 007624 *
1189 007630 *
1190
1191
1192
1193
1194
1195
1196
1197
1198
1199
1200
1201 007632 *
1202 007634 *
1203 007636 *
1204 007640 *
1205 007644 *
1206 007644 *
1207 007646 *
1208 007650 *
1209 007654 *
1210 007654 *
1211 007660 *
1212 007662 *
1213 007664 *
1214 007666 *
1215 007670 *
1216 007672 *
1217 007674 *
1218 007676 *
1219 007700 *
1220 007702 *
1221 007704 *
1222 007706 *
1223 007710 *
1224 007712 *
1225 007714 *
1226 007716 *
1227 007720 *
1228 007722 *
1229 007724 *
1230 007726 *
1231 007730 *
1232 007732 *
1233 007734 *
1234 007736 *
1235 007740 *
1236 007742 *
1237 007744 *
1238 007746 *
1239 007750 *
1240 007752 *
1241 007754 *

MOVE # 1,BREG ;SET THE TYPE OF ERROR.
MOVE BREG,SPAD <0>
CALL SFTERR ;NONE! REPORT ERROR!!
SBR STRTS ;RETURN TO LOOP.

*****
SERVE THE RECEIVE REQUEST
1. DATA TRANSMIT FROM KMC11 TO POP11. THROUGH NPR'S.
2. CONTROL NPR/RR & NPR RATE.
3. REPORT DONE OR ERROR A. NON EX MEM.
B. SOFT ERROR.
*****

RECVE: MOVE SPAD <4>,MEM ;RESTORE MEMORY LOCATION.
        MOVE SPAD <10>,BREG ;IS XMIT FLAG SET?
        BZ 15 ;YES, THEN SERVE THE REQUEST.
        MOVE SPAD <11>,BREG ;IS RECEIVE ONLY FLAG SET?
        BZ 15 ;YES, THEN PROCEED TO SERVE THE REQUEST.
        # 1,BREG ;SET THE TYPE OF ERROR...
        MOVE BREG,SPAD <0>
        CALL SFTERR ;NO, REPORT ERROR!! REC.V. BEFORE XMIT.
        SBR STRTS ;WAIT FOR NEXT REQUEST.
1$: INPI <CSR4>,OUT0 <6> ;LOAD OUTBA <0:7>
    MOVE INPI <CSR4>,SPAD <6> ;LOAD PARALLEL COUNT..
    MOVE INPI <CSR5>,OUT0 <7> ;LOAD OUTBA <8:15>.
    MOVE INPI <CSR5>,SPAD <7> ;LOAD PARALLEL COUNT.
    MOVE INPI <CSR5>,SPAD <0> ;GET GET OUTBA EXTENDED BITS.
    # 300,BREG ;GET THE MASK...
    AND BREG,SPAD <0>,BR SP ;SET IT IN RIGHT PLACE.
    SHFBRT ;SET IT IN RIGHT PLACE.
    SHFBRT ;SET IT IN RIGHT PLACE.
    SHFBRT ;SET IT IN RIGHT PLACE.
    SHFBRT ;SET IT IN RIGHT PLACE.
    MEM,SPAD <1> ;SAVE MEMORY LOCATION.
    # 14,MEM ;GET MASK
    MOVE MEM,SPAD <0> ;IN SPAD <0>.
    AND SPAD <0>,BREG,OUT1 <11> ;SET OUT BA <16:17>
    MOVE SPAD <1>,MEM ;RESTORE MEMORY LOCATION.
    # 0,MEM ;SET THE BUFFER POINTER.
    MOVE SPAD <11>,BREG ;IS RCDLY FLAG SET?
    BZ 25 ;YES, THEN SET LOWER BUFFER ADDRESS.
    # 0,NPR ;CLEAR THE PAGE REGISTER.
    SBR RCVLP ;START THE TRANSMISSION.
    # 2,NPR ;SET POINTER TO GOOD DATA BUFFER.
RCVLP: MOVE MEM,OUT0 <4>,MAR,INC ;LOAD LOW BYTE OF DATA.
        MOVE MEM,OUT0 <3>,MAR,INC ;LOAD HIGH BYTE OF DATA.
        # 021,BREG ;SET
        INPI <10>,SPAD <0> ;WORD XFR, NPR OUT
        OR BREG,SPAD <0>,OUT1 <10> ;IS NPR DONE? & NPR RQ..
1$: INPI <10>,BREG ;IS NPR DONE?
    BZ 25 ;NO, CHECK FOR NON EX MEM.
    SBR ;YES, PREPARE FOR NEXT.
    # 1,BREG ;SET
2$: MOVE INPI <11>,BREG ;IS NON EX MEM. SET?
    OR BZ 45 ;YES,REPORT FATAL ERROR.
```

```
1242 007756 *
1243 007760 *
1244 007762 *
1245 007764 *
1246 007770 *
1247 007772 *
1248 007774 *
1249 007776 *
1250 010000 *
1251 010002 *
1252 010004 *
1253 010006 *
1254 010010 *
1255 010012 *
1256 010014 *
1257 010016 *
1258 010022 *
1259 010024 *
1260 010026 *
1261 010030 *
1262 010032 *
1263 010034 *
1264 010036 *
1265 010040 *
1266 010042 *
1267 010044 *
1268 010046 *
1269 010050 *
1270 010052 *
1271 010054 *
1272 010056 *
1273 010060 *
1274 010062 *
1275 010064 *

4$: SBR # 15 ;NO, WAIT FOR NPR TO CLEAR.
    MOVE # 2,BREG ;SET THE TYPE OF ERROR...
    MOVE BREG,SPAD <0>
    CALL NEXMEM ;REPORT NON EX MEM ERROR.
    SBR STRTS ;WAIT FOR NEXT REQUEST.
3$: SDEC SPAD <15> ;DECREMENT THE COUNT.
    BZ 35 ;BRANCH IF IT WAS 0.
6$: ZINC SPAD <6> ;UPDATE RECEIVE BUFFER
    ZINC SPAD <6> ;UPDATE RECEIVE BUFFER
    SADC SPAD <7> ;UPDATE POINTER.
8$: DC SPAD <6>,BREG ;UPDATE EXTENDED BITS IF CARRY SET.
    MOVE BREG,OUT0 <6> ;SET THE ADDRESS.
    MOVE SPAD <7>,BREG ;SET THE OUTBA..
    MOVE BREG,OUT0 <7>
    CALL NPRATE ;WAIT TO MAINTAIN NPR RATE.
    SBR RCVLP ;ADD THE NEXT XFR.
7$: SDEC SPAD <7> ;SET BACK TO 377.
    # 4,BREG ;LOAD LSR OF EXTENDED ADDRESS.
    INPI <11>,SPAD <0> ;GET EXTENDED ADDRESS BITS.
    BREG,SPAD <0>,OUT1 <11> ;INCREMENT EXTENDED BITS.
5$: SBR # 65 ;WAIT TO MAINTAIN RATE THEN PROCEED.
    SDEC SPAD <16> ;DECREMENT THE MSP'S.
    CALL RCVDNE ;BRANCH IF DONE.
    # 201,BREG ;ELSE, DO THE NEXT.
    INPI <CSR2>,SPAD <0> ;SET RD 0 & RCV DONE BITS IN BREG.
    OR BREG,SPAD <0>,OUT1 <CSR2> ;GET RSEL2.
    INPI <CSR2>,BREG ;SET THE BITS IN RSEL2.
    # 201,BREG ;DUMMY XFR TO CHECK IEO?
    BZ 15 ;BRANCH IF IEO SET.
    SBR STRTS ;WAIT FOR NEXT REQUEST.
1$: # 00,BREG ;SET BR REG. VCH:EX4
    MOVE INPI <CSR11>,SPAD <0> ;GET MICRO-PROCESSOR MISC. REG.
    OR BREG,SPAD <0>,OUT1 <CSR11> ;SET THE BITS IN MISC. REG.
```

```
1276 010066*  
1277 010070*  
1278 010074*  
1279 010078*  
1280 010076*  
1281 010100*  
1282 010105*  
1283 010104*  
1284  
1285  
1286  
1287  
1288  
1289  
1290  
1291  
1292 010106*  
1293 010110*  
1294 010112*  
1295 010114*  
1296 010116*  
1297 010120*  
1298 010124*  
1299 010126*  
1300 010130*  
1301 010134*  
1302 010136*  
1303 010140*  
1304  
1305
```

```
2$: MOVE INP1 <CSR11>,BREG ;IS BR REQ CLEARED?  
BB7 # 277,BREG ;NO SPI ON IT  
MOVE SPAD <1>,MEM ;SET TO CLEAR VCTR:=XX4...  
INP1 <CSR11>,SPAD <0> ;GET UPMS REGISTER...  
AND BREG,SPAD <0>,OUT1 <CSR11> ;CLEAR VCTR:=XX4...  
MOVE # 0,BREG ;GET SET TO CLEAR XMIT FLAG.  
BREG,SPAD <10> ;CLEAR XMIT FLAG  
SBR STRG,SPAD <10> ;WAIT FOR NEXT REQUEST.  
;*****  
;** TRANSMIT THE BUFFER FORM PDP11 TO KMC11  
;** DOES ALL THE FUNCTIONS AS RECEIVE OPERATION AND  
;** ALSO CHECKS THE DATA.  
;*****  
XMIT: MOVE SPAD <4>,MEM ;RESTORE MEMORY LOCATION.  
MOVE INP1 <CSR4>,OUT0 <4> ;GET LOW BYTE OF BUFFER ADDRESS.  
MOVE INP1 <CSR4>,SPAD <6> ;GET PARALLEL ADDRESS IN SCRATCH PAD.  
MOVE INP1 <CSR5>,OUT0 <5> ;GET HIGH BYTE OF BUFFER ADDRESS.  
MOVE INP1 <CSR5>,SPAD <7> ;GET PARALLEL ADDRESS IN SCRATCH PAD.  
MOVE INP1 <CSR7>,SPAD <0> ;GET EXTENDED BITS IN BREG.  
MOVE # 10,BREG ;GET THE MASK...  
AND BREG,SPAD <0>,BR,SP ;*****  
SHFBRT ;POSITION THE BITS.  
SHFBRT ;POSITION THE BITS.  
SHFBRT ;POSITION THE BITS.  
SHFBRT ;POSITION THE BITS.  
MOVE MEM,SPAD <1> ;SAVE MEMORY LOC.  
MOVE # 14,MEM ;
```

```
1306 010142*  
1307 010144*  
1308 010150*  
1309 010150*  
1310 010152*  
1311 010154*  
1312 010158*  
1313 010160*  
1314 010162*  
1315 010164*  
1316 010166*  
1317 010170*  
1318 010172*  
1319 010174*  
1320 010176*  
1321 010200*  
1322 010202*  
1323 010206*  
1324 010210*  
1325 010214*  
1326 010218*  
1327 010222*  
1328 010226*  
1329 010228*  
1330 010232*  
1331 010236*  
1332 010240*  
1333 010244*  
1334 010248*  
1335 010252*  
1336 010256*  
1337 010264*  
1338 010266*  
1339 010270*  
1340 010274*  
1341 010278*  
1342 010280*  
1343 010284*  
1344 010288*  
1345 010292*  
1346 010296*  
1347 010298*  
1348 010298*  
1349 010298*  
1350 010298*  
1351 010302*  
1352 010304*  
1353 010304*  
1354 010306*  
1355 010310*  
1356 010314*  
1357 010318*  
1358 010322*  
1359 010326*  
1360 010328*  
1361 010328*
```

```
MOVE MEM,SPAD <0> ;GET THE MASKING BITS.  
AND BREG,SPAD <0>,OUT1 <10> ;LOAD EXTENDED ADDRESS BITS OF BUFFER  
MOVE SPAD <1>,MEM ;RESTORE MEMCRV.  
MOVE # 0,MLR ;CLEAR MEMORY LOCATION REGISTER  
MOVE # 0,MPR ;CLEAR MEMORY PAGE REGISTER.  
XMTLP: INP1 <CSR10>,SPAD <0> ;GET NPR CONTROL REGISTER.  
MOVE # 001,BREG ;SET NPR RQ. BIT IN BREG.  
OR BREG,SPAD <0>,OUT1 <CSR10> ;SET NPR RQ BIT.  
1$: MOVE INP1 <CSR10>,BREG ;IS NPR DONE?  
BBO # 4 ;NO CHECK FOR NON EX. MEM  
SBR STRG ;YES, PREPARE FOR NEXT  
2$: MOVE INP1 <CSR11>,BREG ;IS NON EX. MEM. SET?  
BBO # 4 ;REPORT FATAL ERROR IF IT IS.  
SBR STRG ;NO WAIT FOR NPR TO CLEAR.  
4$: MOVE # 2,BREG ;SET THE TYPE OF ERROR...  
MOVE BREG,SPAD <0> ;  
CALL NEXMEM ;REPORT NON EX. MEM. ERROR.  
SBR STRG ;WAIT FOR NEXT REQUEST.  
3$: INP0 <0>,MEM MARINC ;LOAD THE DATA IN TO MEMORY.  
MOVE INP0 <1>,MEM MARINC ;LOAD THE DATA IN TO MEMORY.  
SDEC SPAD <15> ;DECREMENT THE COUNT  
BZ # 0 ;BRANCH IF IT WAS 0  
6$: SINC SPAD <6> ;UPDATE THE XMIT  
SINC SPAD <6> ;" " " " " " " "  
SADC SPAD <7> ;BUFFER POINTER.  
MOVE SPAD <6>,BREG ;UPDATE EXTENDED ADDRESS IF CARRY SET.  
8$: MOVE BREG,OUT0 <4> ;SET OUTBA ADDRESS.  
MOVE SPAD <7>,BREG ;SET OUTBA ADDRESS.  
MOVE BREG,OUT0 <5> ;  
CALL NPRATE ;WAIT TO MAINTAIN THE NPR RATE.  
SBR STRG ;DO THE NEXT XPR.  
7$: SDEC SPAD <7> ;SET IT BACK TO 377...  
MOVE # 4,BREG ;LOAD LSB OF EXTENDED ADDRESS.  
MOVE INP1 <CSR10>,SPAD <4> ;LOAD BREG WITH EXTENDED BITS.  
SADD BREG,SPAD <4>,OUT1 <CSR10> ;INCREMENT THE EXTENDED BITS.  
SBR STRG ;WAIT TO MAINTAIN RATE THEN PROCEED.  
5$: SDEC SPAD <16> ;DECREMENT THE MSB'S  
BZ # 0 ;BRANCH IF DONE.  
GS ;ELSE DO THE NEXT  
XMTDNE: MOVE INP1 <CSR6>,SPAD <15> ;GET LOW BYTE OF CHAR. COUNT.  
MOVE # 77,BREG ;LOAD THE MASK.  
MOVE INP1 <CSR7>,SPAD <16> ;GET HIGH BYTE OF CHAR. COUNT.  
AND BREG,SPAD <16>,SPAD <16> ;LOAD HIGH BYTE OF CHAR. COUNT.  
MOVE # 0,BREG ;  
MOVE BREG,SPAD <1> ;SET POINTERS TO DATA  
MOVE BREG,SPAD <2> ;BUFFERS.  
MOVE BREG,SPAD <3> ;SET POINTER TO GOOD.  
MOVE # 2,BREG ;  
MOVE SPAD <1>,MLR ;  
MOVE SPAD <2>,MPR ;  
CKDTLP: MOVE MEM,SPAD <5> ;LOAD GOOD DATA.  
MOVE SPAD <3>,MLR ;  
MOVE SPAD <4>,MPR ;  
SIFEQ SPAD <5>,MEM 1$ ;COMPARE DATA. GO TO 1$ IF GOOD
```



```

1362 010330*      MOVE # 20,BREG          ;SET THE TYPE OF ERROR...
1363 010332*      MOVE BREG,SPAD <0>
1364 010334*      CALL DATERR          ;REPORT DATA ERROR.
1365 010340*      SDEC SPAD <15>      ;DECREMENT COUNT
1366 010342*      RZ 2$              ;BRANCH IF LOW BYTE CIRCLED
1367 010344*      SINC SPAD <3>        ;UPDATE GOOD DATA
1368 010346*      SADC SPAD <4>        ;BUFFER POINTER
1369 010350*      SINC SPAD <1>      ;UPDATE DATA BUFFER POINTER
1370 010352*      SADC SPAD <2>      ;AND LOAD IT IN MAR.
1371 010354*      MOVE SPAD <1>,MLR   ;SET THE MAR...
1372 010356*      MOVE SPAD <2>,MPR   ;SET THE MAR...
  
```

```

1373 010360*      SBR CKDTLP          ;CHECK THE NEXT CHAR.
1374 010362*      SDEC SPAD <16>      ;DECREMENT MSP COUNT.
1375 010364*      RZ 3$              ;BRANCH IF DONE.
1376 010366*      SBR 9$              ;PREPARE FOR THE NEXT.
1377 010370*      MOVE # 205,BREG      ;SET RD 0, XMIT DONE BA/CC 0 /N BREG.
1378 010372*      MOVE INPI <CSR2>,SPAD <0> ;GET BSEL2.
1379 010374*      OR BREG,SPAD <0>,OUT1 <CSR2> ;SET BITS IN BSEL?.
1380 010376*      MOVE INPI <CSR2>,BREG   ;IS IEO SET?
1381 010400*      BB4 4$              ;YES, GO INTERRUPT
1382 010402*      SBR STRTS          ;WAIT FOR NEXT INSTRUCTION. REQUEST.
1383 010404*      MOVE # 300,BREG
1384 010406*      MOVE INPI <CSR11>,SPAD <0> ;
1385 010410*      OR BREG,SPAD <0>,OUT1 <CSR11> ;SET BR RQ,VCTR=XX4
1386 010412*      MOVE INPI <CSR11>,BREG   ;IS BR RQ CLEARED
1387 010414*      BB7 5$              ;NO SPIN ON IT.
1388 010416*      MOVE # 277,BREG      ;SET TO CLEAR VCTR:=XX4.
1389 010420*      MOVE INPI <CSR11>,SPAD <0> ;GET UPMS REGISTER.
1390 010422*      AND BREG,SPAD <0>,OUT1 <CSR11> ;CLEAR VCTR:=XX4.
1391 010424*      SDEC SPAD <10>        ;SET XMIT FLAG
1392 010426*      SBR STRTS          ;GO TO IDLE STATE.
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1408 010430*      NPRATE: MOVE BREG,SPAD <17> ;SAVE THE RETURN ADDRESS.
1409 010432*      MOVE SPAD <13>,BREG   ;GET NPR RATE COUNT...
1410 010434*      MOVE BREG,SPAD <0>    ;
1411 010436*      MOVE SPAD <14>,BREG   ;COUNT MOVE BREG,SPAD <17>
1412 010440*      MOVE BREG,SPAD <1>    ;
1413 010442*      SDEC SPAD <0>        ;DECREMENT THE COUNT
1414 010444*      RZ 2$              ;BRANCH IF LOW BYTE IS COUNTED
1415 010446*      SBR 1$              ;CONTINUE
1416 010450*      SDEC SPAD <1>        ;COUNT HIGH BYTE BY 1.
1417 010452*      RZ 3$              ;RETURN IF DONE.
1418 010454*      SBR 1$              ;CONTINUE.
1419 010456*      SBR SPAD <17> PAGE0 ;RETURN.
1420
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*****
;
; SUBROUTINES 1. NPR RATE CONTROL 2. ERROR REPORTING.
;
*****
;
; 1. NPRATE. THIS ROUTINE DELAYS THE NPR TO
; MAINTAIN THE RATE SET BY THE OPERATOR.
;
; THE SMALLEST UNIT FOR RATE IS 2 MICRO-SECOND WHICH
; IS SIMULATED IN SOFTWARE USING MICRO-PROCESSOR
; MOVE INSTRUCTION. DELAY COUNT IS SET AT THE TIME OF
; LOADING THE MICRO-CODE.
;
NPRATE: MOVE BREG,SPAD <17> ;SAVE THE RETURN ADDRESS.
MOVE SPAD <13>,BREG ;GET NPR RATE COUNT...
MOVE BREG,SPAD <0> ;
MOVE SPAD <14>,BREG ;COUNT MOVE BREG,SPAD <17>
MOVE BREG,SPAD <1> ;
1S: SDEC SPAD <0> ;DECREMENT THE COUNT
RZ 2$ ;BRANCH IF LOW BYTE IS COUNTED
SBR 1$ ;CONTINUE
2S: SDEC SPAD <1> ;COUNT HIGH BYTE BY 1.
RZ 3$ ;RETURN IF DONE.
SBR 1$ ;CONTINUE.
3S: SBR SPAD <17> PAGE0 ;RETURN.
*****
;
; THIS ROUTINE REPORTS ERROR TO PDP11 MONITOR PROGRAM...
; I. SOFT ERROR:- (SPAD <4> BIT0:=1.)
; II. NON EXISTENT MEMORY:- (SPAD <4> BIT1:=1.)
; III. DATA ERROR:- (SPAD <4> BIT4:=1.)
;
  
```


PRTV2 = 000100	235#																			
PRTV3 = 000140	235#																			
PRTV4 = 000200	235#																			
PRTV5 = 000240	235#	191	235#																	
PRTV6 = 000300	235#																			
PRTV7 = 000340	235#																			
PS = 177776	235#																			
PUSH = 057746	235#																			
PUSH2 = 024646	235#																			
RARD = 000200	1#																			
RACT = 000100	1#																			
RANDS = 104417	235#																			
RANNUM = 000054R	209#																			
RBCC = 000001	1#																			
RBF = 000200	1#																			
RBUF1 = 05272R	976#	1045	1051#																	
RBUF11 = 05276R	289#	1024	1059#																	
RBUF2 = 005274R	984#	1046	1054#																	
RBUF21 = 006276R	1025#	1063#																		
RCOLV = 000262R	264#	308#	352*	415	443	806	887													
RCVDNE = 010044R	1266#	1267#																		
RCVLP = 007732R	1231#	1232#	1259																	
RCV = 02426R	655#	656#																		
RCVE = 00805R	1165#	1201#																		
REGO = 007336R	393#	394#	1072#																	
REGOI = 007366R	393#	1077#																		
REGOO = 007370R	394#	1078#																		
REOM = 000070R	1#																			
RESTR = 000322R	228#	285	288#																	
RES1 = 000056R	211#																			
RES2 = 000060R	211#																			
RECLK = 000100R	811#	841	843	849	892	894	910	912	914	916	928#									
RENPR = 000001	1#																			
RNDY = 000020R	1#																			
RNSIZE = 000240R	328#	316*	337*	498	666															
RNRTR = 00324R	228#																			
RNMULV = 000260R	258#	318#	340																	
RSAR0 = 000252R	255#	435*	439																	
RSAR1 = 000255R	255#	436*	440																	
RSADR = 000259R	259#	502*																		
SCAN = 001334R	422#	461#																		
SELECT = 000230R	246#	284	293	358	395	419	473	524*												
SETUP1 = 000744R	246#	362	384#																	
SETUP2 = 000745R	246#	362	384#																	
SETERR = 010460R	1189#	1209	1432#																	
SETERR1 = 010472R	1437#	1439#																		
SFT1 = 000056R	204#	757#																		
SFCNT = 000042R	204#																			
SOPERS = 104406	235#																			
SOPPAS = 000046R	206#	570#																		
SOP11 = 002016R	350#	589#																		
SOP11 = 002016R	350#	589#																		
SOP12 = 002022R	370	572#																		
SOP12 = 002022R	370	572#																		
SOP121 = 002171R	572	599#																		
SPOINT = 000032R	200#																			

SPSIZ = 000040	233																			
SR1 = 000016R	193#	312	314	320	322	323	345	347	354											
SR2 = 000020R	194#																			
SR3 = 000022R	194#																			
SR4 = 000024R	198#																			
START = 000272R	199#	281#																		
STAR1 = 000026R	199#																			
SVRS = 000062R	213#	1142#	1190	1210	1247	1273	1284	1324	1383	1393										
SVR1 = 000064R	214#																			
SVR2 = 000066R	215#																			
SVR3 = 000068R	215#																			
SVR4 = 000072R	219#																			
SVR5 = 000074R	218#																			
SVR6 = 000076R	219#																			
SW1 = 000015	1#																			
SW2 = 000016	1#																			
SYS CNT = 000052R	208#																			
SZMULV = 000256R	254#	316	317	333																
TEMP = 000366R	259#	335	337	338	342	344	359*	371*	372	498*	510*	950*	960*							
TERM = 000000	235#																			
TRPDFD = 000022	235#																			
VA = 000244R	252#	662*	672*	944																
VECTOR = 000010R	186#	385																		
VEC4 = 000100	1#																			
VERIFY = 003624R	367#	866#																		
WASADR = 000104R	222#	703#																		
WCRAN = 003356R	365#	793#																		
WDRAN = 000116R	230#																			
WDT0 = 000114R	229#																			
WMEMRY = 003552R	366#	840#																		
XABO = 006280	1#																			
XACT = 000100	1#																			
XBUF = 004266R	497#	672	845	903	994#															

	1365 1437 1106#	1367 1438 1107#	1374 1439 1108#	1376 1441 1109#	1377 1443 1110#	1382 1458 1111#	1383 1459 1112#	1388 1464 1113#	1393 1469 1114#	1415 1474 1115#	1416 1475 1116#	1418 1477 1117#	1419 1478 1118#
\$\$\$SER = 000001	1109# 1132# 1155# 1178# 1203# 1226# 1249# 1272# 1295# 1318# 1341# 1364# 1387# 1410# 1433# 1456# 1460#	1110# 1133# 1156# 1179# 1202# 1225# 1248# 1271# 1294# 1317# 1340# 1363# 1386# 1409# 1432# 1455# 1460#	1111# 1134# 1157# 1180# 1203# 1226# 1249# 1272# 1295# 1318# 1341# 1364# 1387# 1410# 1433# 1456# 1460#	1112# 1135# 1158# 1181# 1204# 1227# 1250# 1273# 1296# 1319# 1342# 1365# 1388# 1411# 1434# 1457# 1461#	1113# 1136# 1159# 1182# 1205# 1228# 1251# 1274# 1297# 1320# 1343# 1366# 1389# 1412# 1435# 1458# 1462#	1114# 1137# 1160# 1183# 1206# 1229# 1252# 1275# 1298# 1321# 1344# 1367# 1390# 1413# 1436# 1459# 1463#	1115# 1138# 1161# 1184# 1207# 1230# 1253# 1276# 1299# 1322# 1345# 1368# 1391# 1414# 1437# 1460# 1464#	1116# 1139# 1162# 1185# 1208# 1231# 1254# 1277# 1299# 1322# 1345# 1368# 1391# 1414# 1437# 1460# 1464#	1117# 1140# 1163# 1186# 1209# 1232# 1255# 1278# 1301# 1324# 1347# 1370# 1393# 1416# 1439# 1462# 1466#	1118# 1141# 1164# 1187# 1210# 1233# 1256# 1279# 1302# 1325# 1348# 1371# 1394# 1417# 1440# 1463# 1467#	1119# 1142# 1165# 1188# 1211# 1234# 1257# 1280# 1303# 1326# 1349# 1372# 1395# 1418# 1441# 1464# 1468#	1120# 1143# 1166# 1189# 1212# 1235# 1258# 1281# 1304# 1327# 1350# 1373# 1396# 1419# 1442# 1465# 1469#	
.ADC = 010566R	518#	1#	1#	1#	1#	1#	1#	1#	1#	1#	1#	1#	1#
.ADD = 000100	1139#	1139#	1252#	1331#	1369#	1043#	1059#	1063#	1070#	1071#	1072#	1105#	
.ADDW = 000020	1#	1#	1#	1#	1#	1#	1#	1#	1#	1#	1#	1#	1#
.AND = 000260	1177#	1180#	1217#	1225#	1281#	1300#	1308#	1350#	1391#	1467#			
.BR0 = 002000	1239#	1242#	1316#	1319#	1437#								
.BB1 = 002400	1151#	1188#											
.BB4 = 003000	1151#	1272#	1382#	1439#	1458#								
.BB7 = 003400	1147#	1149#	1163#	1165#	1278#	1388#	1464#						
.BC = 001000	1253#	1332#											
.BR = 000400	1247#	1258#	1259#	1152#	1189#	1190#	1209#	1210#	1231#	1240#	1243#	1246#	
	1343#	1346#	1365#	1264#	1267#	1284#	1317#	1320#	1333#	1342#	1337#	1330#	1246#
	1468#			1374#	1377#	1383#	1393#	1416#	1419#	1420#	1441#	1443#	1450#
.BSBRG = 160000	1#	1420#	1468#										
.BSIMG = 100000	1189#	1141#	1142#	1147#	1149#	1150#	1151#	1152#	1163#	1165#	1166#	1183#	1185#
	1247#	1190#	1204#	1206#	1209#	1210#	1219#	1231#	1239#	1240#	1242#	1243#	1246#
	1347#	1249#	1253#	1258#	1259#	1264#	1266#	1267#	1272#	1273#	1278#	1234#	1316#
	1365#	1367#	1374#	1376#	1377#	1382#	1383#	1386#	1393#	1343#	1345#	1346#	1365#
	1437#	1438#	1439#	1441#	1443#	1458#	1459#	1464#	1466#	1415#	1416#	1418#	1419#
.BSMEM = 140000	1#	1#	1#	1#	1#	1#	1#	1#	1#	1#	1#	1#	1#

	1415#	1183#	1185#	1204#	1206#	1229#	1249#	1266#	1328#	1345#	1362#	1367#	1376#
.BZ = 001400	1#	1183#	1185#	1204#	1206#	1229#	1249#	1266#	1328#	1345#	1362#	1367#	1376#
.CO = 000400	1#	1106#	1140#	1143#	1146#	1148#	1154#	1159#	1161#	1162#	1164#	1175#	1176#
.DBR = 000400	1187#	1189#	1203#	1208#	1210#	1211#	1211#	1212#	1212#	1212#	1213#	1214#	1214#
	1246#	1254#	1256#	1258#	1261#	1268#	1271#	1274#	1277#	1279#	1282#	1299#	1302#
	1315#	1318#	1321#	1323#	1333#	1335#	1337#	1340#	1348#	1351#	1355#	1363#	1365#
	1428#	1431#	1434#	1437#	1439#	1440#	1442#	1443#	1446#	1442#	1444#	1447#	1446#
.DDRRSH = 001400	1#	1218#	1219#	1220#	1221#	1301#	1302#	1303#	1304#				
.DDBRSP = 003400	1#	1180#	1217#	1300#									
.DD = 003000	1141#	1168#	1174#	1181#	1182#	1185#	1217#	1225#	1237#	1263#	1270#	1276#	1281#
	1300#	1248#	1260#	1262#	1323#	1339#	1360#	1366#	1375#	1446#	1463#	1467#	1471#
	1#	1137#	1182#	1183#	1184#	1185#	1186#	1202#	1223#	1226#	1293#	1306#	1309#
.DEC = 000160	1325#	1326#	1365#	1362#									
.DMEM = 002400	1#	1183#	1185#	1185#	1185#	1186#	1202#	1223#	1226#	1226#	1293#	1306#	1309#
.DNOP = 000000	1#	1228#	1129#	1130#	1131#	1132#	1133#	1134#	1135#	1136#	1211#	1213#	1233#
.DOUT0 = 002000	1234#	1257#	1257#	1296#	1296#	1334#	1336#	1334#	1335#	1336#	1211#	1213#	1233#
.DOUT1 = 001000	1#	1158#	1161#	1121#	1124#	1123#	1124#	1125#	1126#	1127#	1145#	1155#	1156#
	157#	1391#	1446#	1448#	1450#	1452#	1454#	1456#	1462#	1467#	1314#	1342#	1350#
.DSP = 003000	1#	1109#	1110#	1111#	1112#	1113#	1114#	1115#	1116#	1117#	1118#	1133#	1139#
	1141#	1145#	1150#	1174#	1176#	1177#	1179#	1180#	1181#	1188#	1205#	1212#	1214#
	1215#	1217#	1222#	1274#	1276#	1277#	1275#	1278#	1279#	1280#	1251#	1252#	1262#
	1263#	1265#	1269#	1270#	1275#	1276#	1280#	1281#	1283#	1295#	1297#	1298#	1306#
	1305#	1307#	1308#	1312#	1314#	1322#	1327#	1329#	1330#	1331#	1333#	1334#	1342#
	1344#	1347#	1349#	1359#	1362#	1385#	1385#	1386#	1386#	1389#	1391#	1392#	1396#
	1414#	1417#	1435#	1445#	1446#	1461#	1462#	1466#	1467#	1362#	1409#	1411#	1413#
.DO = 000400	1#	1144#	1146#	1148#	1160#	1162#	1164#	1174#	1176#	1179#	1211#	1212#	1213#
.FO = 000020	1214#	1215#	1236#	1238#	1241#	1252#	1259#	1271#	1275#	1277#	1280#	1284#	1285#
	1296#	1297#	1298#	1312#	1315#	1318#	1325#	1326#	1341#	1347#	1349#	1379#	1381#
	1385#	1387#	1398#	1445#	1457#	1461#	1463#	1466#					
	1#	1138#	1250#	1251#	1329#	1330#	1368#	1370#					
.INC = 000060	1#	1109#	1110#	1111#	1112#	1113#	1114#	1115#	1116#	1117#	1118#	1119#	1120#
.LORNI = 000240	1121#	1122#	1123#	1124#	1125#	1126#	1127#	1128#	1129#	1130#	1131#	1132#	1133#
.MINUS = 000360	1134#	1135#	1136#	1138#	1139#	1140#	1145#	1155#	1157#	1159#	1159#	1161#	1177#
.MO = 004000	1180#	1186#	1188#	1202#	1203#	1208#	1208#	1217#	1218#	1219#	1220#	1225#	1225#
.OR = 000300	1228#	1237#	1245#	1248#	1250#	1250#	1251#	1252#	1254#	1255#	1255#	1257#	1260#
.PLUS = 000000	1263#	1265#	1269#	1270#	1276#	1281#	1283#	1293#	1300#	1301#	1302#	1304#	1308#
.SBR = 060000	1309#	1314#	1327#	1329#	1353#	1350#	1351#	1333#	1334#	1335#	1336#	1339#	1342#
	1344#	1350#	1352#	1353#	1354#	1358#	1357#	1358#	1360#	1361#	1364#	1366#	1366#
	1369#	1370#	1371#	1372#	1373#	1375#	1380#	1386#	1391#	1392#	1409#	1410#	1412#
	1412#	1413#	1414#	1417#	1435#	1436#	1436#	1446#	1447#	1448#	1449#	1450#	1451#
.SELA = 000200	1#	1140#	1186#	1202#	1203#	1205#	1226#	1228#	1254#	1256#	1293#	1309#	1333#
.SELB = 000220	1335#	1357#	1358#	1360#	1361#	1372#	1373#	1410#	1412#	1420#	1436#	1447#	1449#
	1451#	1453#	1455#	1468#	1468#	1469#	1469#						
	1#	1109#	1110#	1111#	1112#	1113#	1114#	1115#	1116#	1117#	1118#	1119#	1120#

1121	1122	1123	1124	1125	1126	1127	1128	1129	1130	1131	1132	1133
1134	1135	1136	1137	1138	1139	1140	1141	1142	1143	1144	1145	1146
1221	1222	1223	1224	1225	1226	1227	1228	1229	1230	1231	1232	1233
1303	1307	1322	1334	1326	1352	1353	1354	1356	1301	1302	1303	1304
1413	1435	1448	1450	1452	1454	1456			1359	1364	1409	1411
.SIMM = 000000	1# 1106	1107	1108	1137	1143	1154	1159	1175	1178	1182	1184	1187
	1189	1207	1209	1216	1223	1227	1230	1232	1244	1246	1258	1261
	1268	1274	1279	1282	1289	1306	1310	1311	1321	1323	1337	1340
	1348	1351	1355	1363	1365	1378	1384	1389	1440	1442	1444	1460
	1# 1325	1326										
	1# 1144	1146	1148	1160	1162	1164	1174	1176	1179	1211	1212	1213
.SINO = 020000	1214	1215	1238	1241	1262	1269	1271	1272	1277	1290	1294	1295
.SINI = 120000	1296	1297	1298	1319	1315	1318	1341	1347	1349	1379	1381	1387
	1390	1445	1457	1461	1463	1466						
	1# 1181	1183	1185	1222	1224	1233	1234	1305	1307	1359	1362	
.SMEH = 040000	1#											
.SUBMC = 000040	1#											
.SUBIC = 000340	1#											
.SUB2C = 000360	1#	1183	1185	1362								
.SO = 020000	1#											
.XOR = 000320	1#											
.S = 160617	1#											
	1106#	1107#	1108#	1109#	1110#	1111#	1112#	1113#	1114#	1115#	1116#	1117#
	1119#	1120#	1121#	1122#	1123#	1124#	1125#	1126#	1127#	1128#	1129#	1130#
	1132#	1133#	1134#	1135#	1136#	1137#	1138#	1139#	1140#	1141#	1142#	1143#
	1146#	1146#	1147#	1148#	1149#	1150#	1151#	1152#	1153#	1154#	1155#	1156#
	1159#	1160#	1161#	1162#	1163#	1164#	1165#	1166#	1167#	1168#	1169#	1170#
	1179#	1180#	1181#	1182#	1183#	1184#	1185#	1186#	1187#	1188#	1189#	1190#
	1203#	1204#	1205#	1206#	1207#	1208#	1209#	1210#	1211#	1212#	1213#	1214#
	1216#	1217#	1218#	1219#	1220#	1221#	1222#	1223#	1224#	1225#	1226#	1227#
	1233#	1234#	1235#	1236#	1237#	1238#	1239#	1240#	1241#	1242#	1243#	1244#
	1246#	1247#	1248#	1249#	1250#	1251#	1252#	1253#	1254#	1255#	1256#	1257#
	1259#	1260#	1261#	1262#	1263#	1264#	1265#	1266#	1267#	1268#	1269#	1270#
	1272#	1273#	1274#	1275#	1276#	1277#	1278#	1279#	1280#	1281#	1282#	1283#
	1293#	1294#	1295#	1296#	1297#	1298#	1299#	1300#	1301#	1302#	1303#	1304#
	1310#	1311#	1312#	1313#	1314#	1315#	1316#	1317#	1318#	1319#	1320#	1321#
	1323#	1324#	1325#	1326#	1327#	1328#	1329#	1330#	1331#	1332#	1333#	1334#
	1336#	1337#	1338#	1339#	1340#	1341#	1342#	1343#	1344#	1345#	1346#	1347#
	1349#	1350#	1351#	1352#	1353#	1354#	1355#	1356#	1357#	1358#	1359#	1360#
	1362#	1363#	1364#	1365#	1366#	1367#	1368#	1369#	1370#	1371#	1372#	1373#
	1375#	1376#	1377#	1378#	1379#	1380#	1381#	1382#	1383#	1384#	1385#	1386#
	1388#	1389#	1390#	1391#	1392#	1393#	1409#	1410#	1411#	1412#	1413#	1414#
	1416#	1417#	1418#	1419#	1420#	1425#	1435#	1436#	1437#	1438#	1439#	1440#
	1443#	1444#	1445#	1446#	1447#	1448#	1449#	1450#	1451#	1452#	1453#	1454#
	1456#	1457#	1458#	1459#	1460#	1461#	1462#	1463#	1464#	1465#	1466#	1467#
	1#											
.2A = 000120	1#											
.2AWC = 000140	1#											
	1#											
.ABS. 000000 000												
010566 001												

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0
 XKMCB0, XKMCB0/SOL/CRF:SYM=DDXCOM, XKMCB0
 RUN-TIME: 20 22 1 SECONDS

RUN-TIME RATIO: 174/44=3.9
 CORE USED: 9K (17 PAGES)

DIAGNOSTIC ENGINEERING

2500

digital

DECO DEPO SUBMISSION

FOR RELEASE ENG. USE
 NEW CHANGE DELETE

PRODUCT IDENTIFICATION

MD	LIBRARY	PRODUCT NUMBER	REV	PATCH	ECO TALLY	PRODUCT DATE	STATUS	DISTRIBUTION	1ST COPY - RIGHT YEAR	LAST COPY - RIGHT YEAR
	ZZ	CXKMC	B	1	Q/	19 APR 79	OBSOLETE	X G R	1976	1979

TITLE CXKMCB1 KMC-11 MODULE

AUTHOR D. BUTENHOF MAINTAINING GROUP DEC/X Supt GRP MAINTAINER D. BUTENHOF SUBMITTING ENGINEER D. BUTENHOF

PRODUCT COMPONENTS

CK	DESCRIPTION	PRODUCT NO.	REV	CK	DESCRIPTION	PRODUCT NO.	REV
	DOCUMENT				INDEX		
	LISTING				SOURCE MEDIA		
	OBJECT MEDIA				TEST MEDIA		
X	DECO	AF-E950B-M1					

PRODUCTS OBSOLETE (other than previous version)

LIBRARY	PRODUCT NUMBER	REV	LIBRARY	PRODUCT NUMBER	REV	LIBRARY	PRODUCT NUMBER	REV
MD			MD			MD		

PRODUCT CHARACTERISTICS

PROCESSORS PRODUCT OPERATES WITH (Enter all applicable 2-digit codes representing the Processor the product operates with. See separate instructions.)

OPERATIONAL CODES (Enter all applicable 2-digit codes that describe the product. See separate instructions.)

ACT/APT/XXDP	EXT	ACT SEQ NUMBER	ACT/XXDP COMPATIBLE?	APT COMPATIBLE?	1ST PASS RUN TIME	SUBSEQUENT PASS RUN TIME
INFORMATION FIELD			<input checked="" type="checkbox"/> Y <input type="checkbox"/> N	<input checked="" type="checkbox"/> Y <input type="checkbox"/> N	SECONDS	SECONDS

DECO/DEPO INFORMATION

PROBLEM REPORTS CLOSED: _____

DEVICE AFFECTED DEC/X11 MULTIMEDIA AFFECTED? YES NO

KIT NUMBERS	ZJ130-RB					
	ZJ129-RZ, FR					

PROBLEM:
 ERRORS WHEN BUFFERS OVERLAP 32K BOUNDARY, DUE TO MICROCODE BUGS.

SOLUTION:
 PATCH THE FOLLOWING MODULE LOCATIONS

DEPO PATCH AREA

CHANGE LOC	FROM	TO	CHANGE LOC	FROM	TO
10024	63167	63207			
10246	63167	63207			
10254	61014	61010			

SUBMITTING ENGINEER <i>D. Butenhof</i>	MANUFACTURING ENGINEER <i>E. Casella</i>	SUPPORT ENGINEER	CHARGE DECO/DEPO TO DISCRETE PROJECT NUMBER <i>99805460</i>
DATE: 19 APR 79	DATE: 25-APR-79	DATE:	
MAINTAINER <i>D. Butenhof</i>	FIELD SERVICE <i>D. Craft</i>	WAIVERING MANAGER	COORDINATION NO. <i>MC 3087</i>
DATE: 25 Apr. 79	DATE:	DATE:	